

TEST DATA

for

TRANSISTORS *and* **CRYSTAL DIODES**

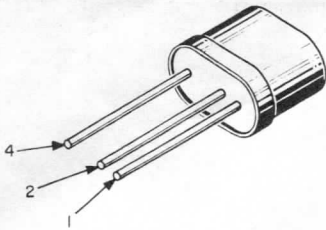
For use with Precision Model 660 Tube and Transistor Tester

IMPORTANT

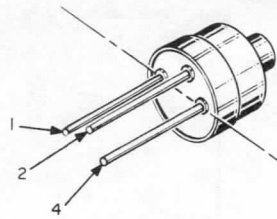
*TRANSISTORS are listed on the YELLOW sheets
CRYSTAL DIODES are listed on the BLUE sheets*

PRECISION APPARATUS COMPANY, INC.

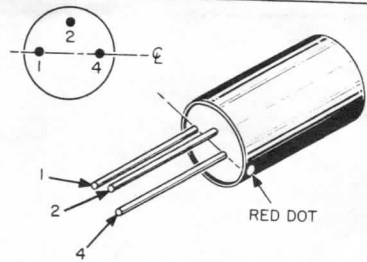
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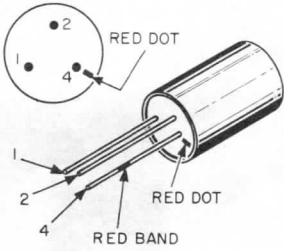
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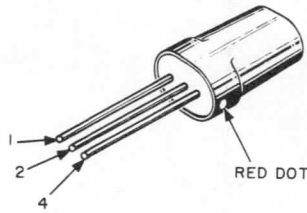
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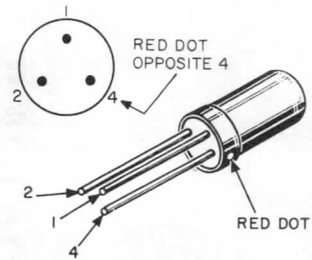
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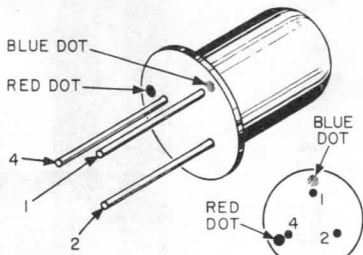
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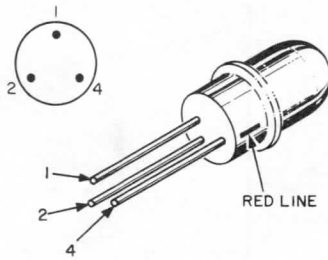
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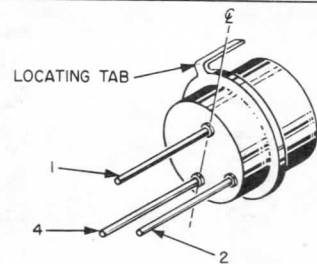
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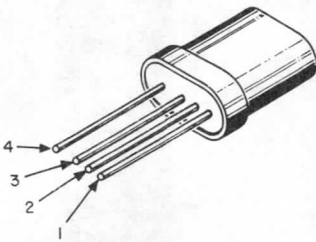
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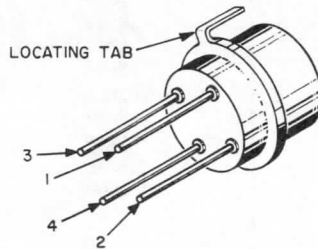
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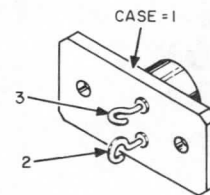
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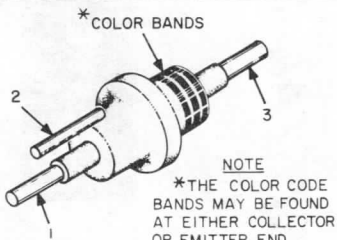
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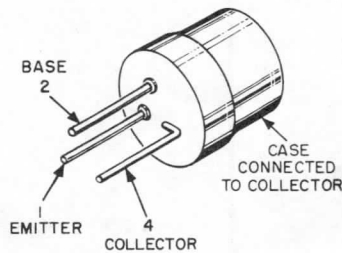
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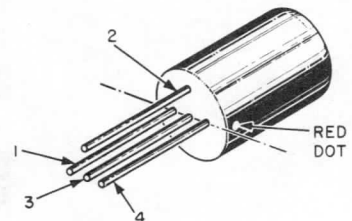
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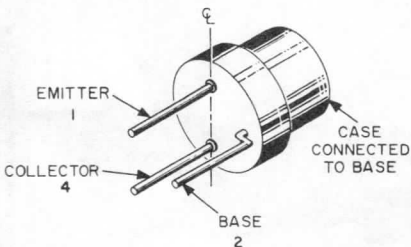
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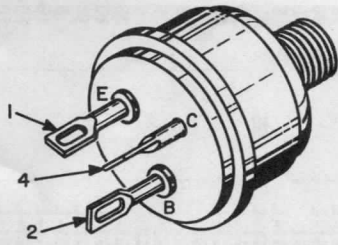
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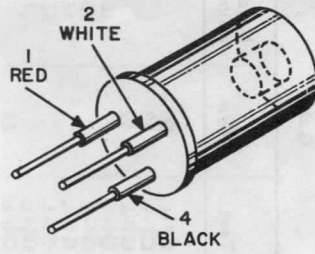
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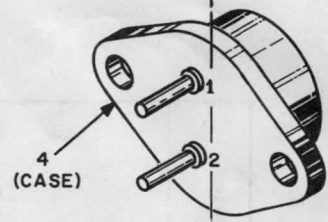
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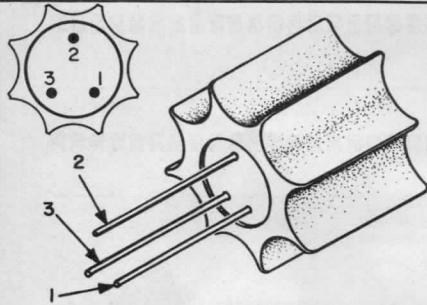
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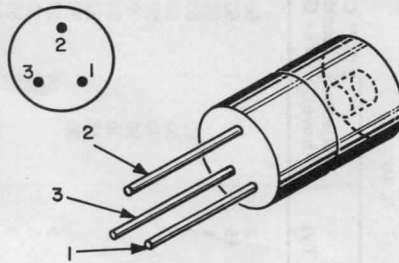
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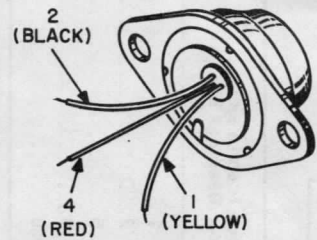
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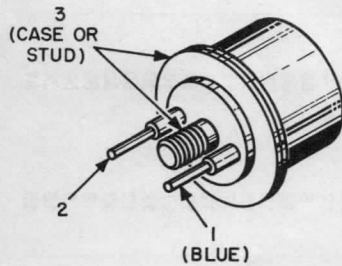
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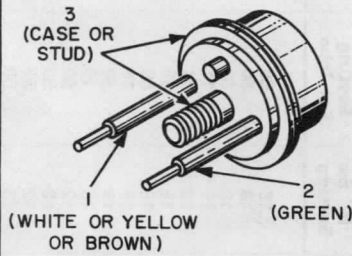
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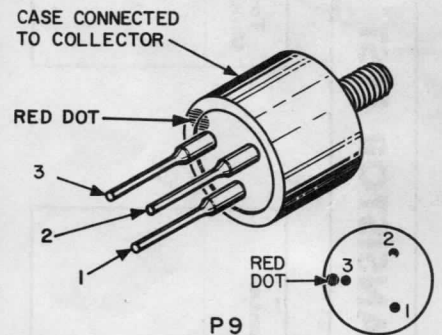
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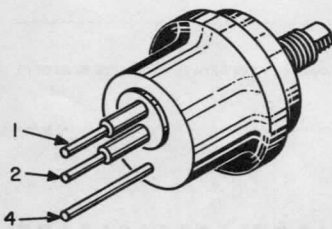
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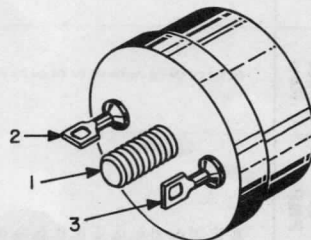
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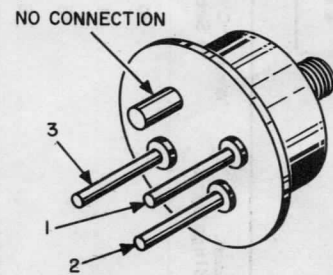
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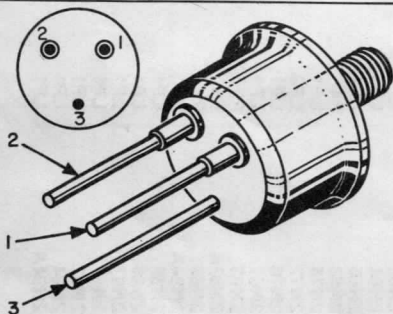
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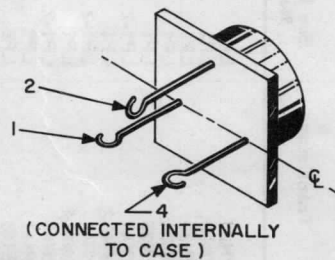
P 11



P 12



P 13



P 14

TRANSISTOR TEST DATA

To Read True Gain, Multiply Meter Readings by:	When "C" Control is Set to:
1.....	50
2.....	31
5.....	15
10.....	6

To Read True IcBO in μ a, Multiply Meter Readings by:	When "C" Control is Set to:
1.....	50
2.....	31
5.....	18
10.....	11
200.....	0

Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For IcBO	Short Test Reject Above:	IcBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
2N27	LP1	NPN	2	4	18	50	4	3		50	G1-L	50	
2N28	LP1	NPN	2	4	18	50	4	15		50	G1-L	32	
2N29	LP1	NPN	2	4	18	50	4	4		50	G1-L	49	
2N34	LP1	PNP	2	4	19	11	6		60	50	G1-L		10
2N35	LP1	NPN	2	4	19	11	6		60	50	G1-L		10
2N43	LP1-2	PNP	2	4	19	18	4		30	50	G1-L		15
2N43A	LP1	PNP	2	4	19	50	4		30	50	G1-L		15
2N44	LP1-2	PNP	2	4	19	18	4		30	50	G1-L		7
2N44A	LP1	PNP	2	4	19	50	4		48	50	G1-L	23	
2N45	LP1-2	PNP	2	4	19	18	4		30	50	G1-L		5
2N47	LP8	PNP	2	4	12	50	4	5		50	G1-L	40	
2N48	LP8	PNP	2	4	12	50	4	5		50	G1-L	32	
2N49	LP8	PNP	2	4	12	50	4	5		50	G1-L	40	
2N59	LP2	PNP	2	4	15	50	8		30	31	G1-L	55	
2N60	LP2	PNP	2	4	15	50	8		30	50	G1-L	90	
2N61	LP2	PNP	2	4	15	50	8		30	50	G1-L	45	
2N63	LP1	PNP	2	4	15	31	4		38	50	G1-L		7
2N64	LP1	PNP	2	4	15	31	4		38	50	G1-L		15
2N65	LP1	PNP	2	4	15	31	4		38	50	G1-L		30
2N68	P4	PNP	2	3	17	0	44		50	50	G2		5
2N76	LP1	PNP	2	4	8	50	4		30	50	G1-L		5
2N77	LP3	PNP	2	4	12	50	4		30	50	G1-L	55	
2N78	LP1	NPN	2	4	13	50	4		18	50	G1-L		10
2N81	LP1	PNP	2	4	15	50	4		48	50	G1-L		10
2N83	P9	PNP	2	3	17	11	8		60	50	G2		4
2N83A	P9	PNP	2	3	17	11	8		60	50	G2		4
2N84	P9	PNP	2	3	17	11	8		60	50	G2		6
2N84A	P9	PNP	2	3	17	11	8		60	50	G2		6
2N85	LP1	PNP	2	4	12	50	4		30	50	G1-L		20
2N86	LP1	PNP	2	4	12	50	4		30	50	G1-L		10
2N87	LP1	PNP	2	4	12	50	4		30	50	G1-L		10
2N91	LP1	PNP	2	4	9	50	4		45	50	G1-L		8
2N92	LP1	PNP	2	4	9	50	4		30	50	G1-L		15
2N94	LP1	NPN	2	4	15	31	4		38	50	G1-L		10
2N94A	LP1	NPN	2	4	15	31	4		38	50	G1-L		10

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Reject Above:	Typ		Reject Above:	Test	Typ
2N95	P4	NPN	2	3	17	0	44		50	50	G2		5
2N97	LP1	NPN	2	4	17	50	4		60	50	G1-L	13	3
2N97A	LP1	NPN	2	4	17	50	2		15	50	G1-L	13	3
2N98	LP1	NPN	2	4	19	50	4		60	50	G1-L		10
2N98A	LP1	NPN	2	4	8	50	4		30	50	G1-L		12
2N99	LP1	NPN	2	4	19	50	4		60	50	G1-L		10
2N100	LP1	NPN	2	4	17	50	4		60	31	G1-L		25
2N101	P5	PNP	2	3	17	0	44		50	50	G2		5
2N102	P5	NPN	2	3	17	0	44		50	50	G2		5
2N103	LP1	NPN	2	4	18	18	4		30	50	G1-L	4	
2N104	LP1	PNP	2	4	12	50	4		30	50	G1-L	44	
2N105	LP4	PNP	2	4	12	50	4		15	50	G1-L	45	
2N106	LP1	PNP	2	4	9	50	4		36	50	G1-L		12
2N107	LP1	PNP	2	4	8	50	4		30	50	G1-L	19	3
2N109	LP1	PNP	2	4	17	50	4		42	50	G1-L		22
2N111	LP1	PNP	2	4	12	50	4		15	50	G1-L		8
2N111A	LP1	PNP	2	4	12	50	4		15	50	G1-L		8
2N112	LP1	PNP	2	4	12	50	4		15	50	G1-L		8
2N112A	LP1	PNP	2	4	12	50	4		15	50	G1-L		8
2N113	LP1	PNP	2	4	12	50	4		15	50	G1-L		10
2N114	LP1	PNP	2	4	12	50	4		15	50	G1-L		10
2N115	P12	PNP	2	3	10	50	4		60	50	G2	40	
2N117	LP1	NPN	2	4	17	50	4		30	50	G1-L		5
2N118	LP1	NPN	2	4	17	50	4		30	50	G1-L		10
2N118A	LP1	NPN	2	4	17	50	4		30	50	G1-L		20
2N119	LP1	NPN	2	4	17	50	4		30	50	G1-L		20
2N123	LP1	PNP	2	4	15	50	4		18	50	G1-L		15
2N124	LP1	NPN	2	4	8	50	4		6	50	G1-L		6
2N125	LP1	NPN	2	4	8	50	4		6	50	G1-L		12
2N126	LP1	NPN	2	4	8	50	4		6	50	G1-L		24
2N127	LP1	NPN	2	4	8	50	4		6	31	G1-L		25
2N128	LP8	PNP	2	4	8	50	4		9	50	G1-L		10
2N129	LP8	PNP	2	4	8	50	4		9	50	G1-L		6
2N130	LP5	PNP	2	4	15	31	4		38	50	G1-L		7
2N130A	LP5	PNP	2	4	15	31	4		38	50	G1-L		7
2N131	LP5	PNP	2	4	15	31	4		38	50	G1-L		15
2N131A	LP5	PNP	2	4	15	31	4		38	50	G1-L		15
2N132	LP5	PNP	2	4	15	31	4		38	50	G1-L		30
2N132A	LP5	PNP	2	4	15	31	4		38	50	G1-L		30
2N133	LP5	PNP	2	4	9	50	4		36	50	G1-L		12
2N133A	LP5	PNP	2	4	9	50	4		36	50	G1-L		12
2N135	LP1	PNP	2	4	9	50	4		15	50	G1-L	20	3
2N136	LP1	PNP	2	4	9	50	4		15	50	G1-L		8
2N137	LP1	PNP	2	4	9	50	4		15	50	G1-L		15
2N138	LP5	PNP	2	4	15	31	4		38	50	G1-L		22
2N138A	LP5	PNP	2	4	15	31	4		38	50	G1-L		22
2N139	LP1	PNP	2	4	12	50	4		18	50	G1-L	45	
2N140	LP1	PNP	2	4	12	50	4		18	50	G1-L	48	
2N141	P4	PNP	2	3	20	0	26		50	50	G2		5
2N142	P4	NPN	2	3	20	0	26		50	50	G2		5
2N143	P4	PNP	2	3	20	0	26		50	50	G2		5
2N144	P4	NPN	2	3	20	0	26		50	50	G2		5
2N151	P5	PNP	2	3	17	11	60		50	50	G2	30	

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
2N154	P5	PNP	2	3	13	18	6		60	50	G2		15
2N155	P3	PNP	2	4	18	0	44		50	50	G2		12
2N156	P2	PNP	2	4	18	0	18		15	50	G2		12
2N157	P3	PNP	2	4	19	0	10		10	50	G2		10
2N157A	P3	PNP	2	4	21	0	8		10	50	G2		10
2N158	P2	PNP	2	4	20	0	10		15	50	G2		10
2N158A	P2	PNP	2	4	11	0	36		10	50	G2		11
2N160*	LP1	NPN	2	4	19	50	4		30	50	G1-L		4
*Maximum Emitter to Base Voltage rating = 1 Volt, therefore DO NOT use more than 1 volt to measure IEBO.													
2N160A	LP1	NPN	2	4	19	50	4		30	50	G1-L		4
2N161*	LP1	NPN	2	4	19	50	4		30	50	G1-L		10
*Maximum Emitter to Base Voltage rating = 1 Volt, therefore DO NOT use more than 1 volt to measure IEBO.													
2N161A	LP1	NPN	2	4	19	50	4		30	50	G1-L		10
2N162*	LP1	NPN	2	4	19	50	4		30	50	G1-L		10
*Maximum Emitter to Base Voltage rating = 1 Volt, therefore DO NOT use more than 1 volt to measure IEBO.													
2N162A	LP1	NPN	2	4	19	50	4		30	50	G1-L		10
2N163*	LP1	NPN	2	4	19	50	4		30	50	G1-L		20
*Maximum Emitter to Base Voltage rating = 1 Volt, therefore DO NOT use more than 1 volt to measure IEBO.													
2N163A	LP1	NPN	2	4	19	50	4		30	50	G1-L		20
2N164A	LP9	NPN	2	4	13	50	4		15	50	G1-L		10
2N165	LP9	NPN	2	4	13	50	4		15	50	G1-L		18
2N166	LP2	NPN	2	4	9	50	4		15	50	G1-L	32	
2N167	LP1	NPN	2	4	13	50	2		5	50	G1-L		8
2N168	LP1	NPN	2	4	13	50	4		15	50	G1-L		5
2N168A	LP1	NPN	2	4	13	50	4		15	50	G1-L		10
2N169	LP1	NPN	2	4	13	50	4		15	50	G1-L		18
2N169A	LP1	NPN	2	4	13	50	4		15	50	G1-L		18
2N170	LP1	NPN	2	4	8	50	4		15	50	G1-L	20	3
2N173	P8	PNP	2	3	20	0	44		80	50	G2		20
2N174	P8	PNP	2	3	20	0	44		80	50	G2		15
2N174A	P8	PNP	2	3	20	0	40		80	50	G2		20
2N175	LP1	PNP	2	4	17	50	4		36	50	G1-L	65	
2N176	P3	PNP	2	4	18	0	30		30	50	G2		12
2N178	P3	PNP	2	4	18	0	30		30	50	G2		8
2N179	P3	PNP	2	4	18	0	30		30	50	G2		5
2N180	LP5	PNP	2	4	18	50	4		60	50	G1-L		20
2N181	LP5	PNP	2	4	18	50	4		60	50	G1-L		20
2N182	LP5	NPN	2	4	15	50	4		30	50	G1-L		8
2N183	LP5	NPN	2	4	15	50	4		30	50	G1-L		8
2N184	LP5	NPN	2	4	15	50	4		30	50	G1-L		8
2N185	LP1	PNP	2	4	15	50	4		45	50	G1-L		17
2N186	LP1	PNP	2	4	17	50	4		48	50	G1-L		10
2N186A	LP1	PNP	2	4	17	50	4		48	50	G1-L		10
2N187	LP1	PNP	2	4	17	50	4		48	50	G1-L		15
2N187A	LP1	PNP	2	4	17	50	4		48	50	G1-L		15
2N188	LP1	PNP	2	4	17	50	4		48	50	G1-L		22
2N188A	LP1	PNP	2	4	17	50	4		48	50	G1-L		22
2N189	LP1	PNP	2	4	17	50	4		48	50	G1-L		10
2N190	LP1	PNP	2	4	17	50	4		48	50	G1-L		15

Continued on Next Page

Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
2N191	LP1	PNP	2	4	17	50	4		48	50	G1-L		22
2N192	LP1	PNP	2	4	17	50	4		48	50	G1-L		30
2N193	LP1	NPN	2	4	13	31	4		38	50	G1-L		10
2N194	LP1	NPN	2	4	13	31	4		38	50	G1-L		10
2N195	LP2	PNP	2	4	12	50	4		30	31	G1-L		25
2N196	LP2	PNP	2	4	18	31	4		75	50	G1-L		25
2N197	LP2	PNP	2	4	18	31	4		75	50	G1-L		20
2N198	LP2	PNP	2	4	18	31	4		75	50	G1-L		15
2N199	LP2	PNP	2	4	18	31	4		75	50	G1-L		8
2N200	LP2	PNP	2	4	18	31	4		75	50	G1-L		15
2N204	LP2	PNP	2	4	18	31	4		75	50	G1-L		25
2N205	LP2	PNP	2	4	18	31	4		75	50	G1-L		8
2N206	LP3	PNP	2	4	17	50	4		30	50	G1-L	47	
2N207	LP8	PNP	2	4	12	50	4		30	50	G1-L		18
2N207A	LP8	PNP	2	4	12	50	4		30	50	G1-L		18
2N207B	LP8	PNP	2	4	12	50	4		30	50	G1-L		18
2N211	LP1	NPN	2	4	12	31	4		45	50	G1-L		10
2N212	LP1	NPN	2	4	12	31	4		45	50	G1-L		10
2N213	LP1	NPN	2	4	17	11	8		60	31	G1-L	75	
2N214	LP1	NPN	2	4	19	11	6		60	50	G1-L		25
2N215	LP3	PNP	2	4	12	50	4		30	50	G1-L	44	
2N216	LP1	NPN	2	4	13	31	4		45	50	G1-L		10
2N217	LP1-3	PNP	2	4	17	50	4		42	50	G1-L		22
2N218	LP3	PNP	2	4	12	50	4		18	50	G1-L	45	
2N219	LP3	PNP	2	4	12	50	4		18	50	G1-L	48	
2N220	LP3	PNP	2	4	17	50	4		36	50	G1-L	65	
2N223	LP8	PNP	2	4	11	18	6		36	50	G1-L		20
2N224	LP8	PNP	2	4	12	31	4		38	50	G1-L		15
2N225	LP8	PNP	2	4	12	31	4		38	50	G1-L		15
2N226	LP8	PNP	2	4	12	31	4		38	50	G1-L		10
2N227	LP8	PNP	2	4	12	31	4		38	50	G1-L		10
2N228	LP1	NPN	2	4	19	11	6		60	50	G1-L		25
2N229	LP1	NPN	2	4	9	11	18		60	50	G1-L		5
2N233	LP1	NPN	2	4	11	18	8		60	50	G1-L	5	
2N233A	LP1	NPN	2	4	13	11	8		45	50	G1-L	5	
2N234	P3	PNP	2	4	17	0	58	70		50	G2	25	
2N234A	P3	PNP	2	4	17	0	20		10	50	G2	25	
2N235	P3	PNP	2	4	17	0	20		10	50	G2	50	10
2N235A	P3	PNP	2	4	17	0	20		10	50	G2	60	
2N235B	P3	PNP	2	4	17	0	70		100	50	G2	40	
2N236	P3	PNP	2	4	17	0	20		10	50	G2	40	
2N236A	P3	PNP	2	4	17	0	20		10	50	G2	40	
2N236B	P3	PNP	2	4	17	0	14		10	50	G2	60	
2N238	LP1	PNP	2	4	15	50	4		60	50	G1-L		40
2N240	LP8	PNP	2	4	8	50	4		9	50	G1-L		8
2N241	LP1	PNP	2	4	17	50	4		48	50	G1-L		30
2N241A	LP1	PNP	2	4	17	50	4		48	50	G1-L		30
2N242	P3	PNP	2	4	19	0	36		50	50	G2		12
2N243	LP1	NPN	2	4	17	50	2		3	50	G1-L		5
2N244	LP1	NPN	2	4	17	50	2		3	50	G1-L		12
2N247	LP10	PNP	2	4	17	50	4		48	50	G1-L	60	
2N248	LP1	PNP	2	4	12	50	4		30	50	G1-L		10
2N249	LP14	PNP	2	4	17	50	4		30	50	G1-L	45	

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
2N250	P3	PNP	2	4	18	0	12		10	50	G2		15
2N251	P3	PNP	2	4	19	0	12		20	50	G2		15
2N255	P3	PNP	2	4	13	0	64		50	50	G2	30	
2N256	P3	PNP	2	4	18	0	44		50	50	G2	30	
2N257	P3	PNP	2	4	19	0	18		20	50	G2	60	
2N260	LP5	PNP	2	4	9	50	2		2	50	G1-L	16	
2N260A	LP5	PNP	2	4	9	50	2		2	50	G1-L	16	
2N261	LP5	PNP	2	4	9	50	2		2	50	G1-L	10	
2N262	LP5	PNP	2	4	9	50	2		2	50	G1-L	20	
2N262A	LP5	PNP	2	4	9	50	2		2	50	G1-L	20	
2N265	LP1	PNP	2	4	17	50	4		48	31	G1-L		24
2N268	P3	PNP	2	4	20	0	12		20	50	G2	60	
2N269	LP3	PNP	2	4	12	50	4		15	50	G1-L		18
2N270	LP1	PNP	2	4	17	50	4		48	31	G1-L	35	
2N271	LP1	PNP	2	4	12	50	4		15	50	G1-L	45	
2N271A	LP1	PNP	2	4	12	50	4		15	50	G1-L	45	
2N272	LP1	PNP	2	4	15	31	4		38	50	G1-L		30
2N274	LP15	PNP	2	4	17	50	4		48	50	G1-L	60	
2N277	P8	PNP	2	3	17	0	56		50	50	G2		33
2N278	P8	PNP	2	3	15	0	56		50	50	G2	50	
2N279	LP1	PNP	2	4	8	50	4		36	50	G1-L		10
2N280	LP1	PNP	2	4	8	50	4		36	50	G1-L		15
2N281	LP1	PNP	2	4	11	50	4		30	50	G1-L		13
2N282	LP1	PNP	2	4	11	50	4		30	50	G1-L		13
2N283	LP1	PNP	2	4	17	50	4		27	50	G1-L		15
2N284	LP1	PNP	2	4	11	50	4		30	50	G1-L		13
2N284A	LP1	PNP	2	4	11	50	4		30	50	G1-L		13
2N285	P3	PNP	2	4	17	0	14		10	31	G2		20
2N285A	P3	PNP	2	4	17	0	14		10	31	G2		20
2N290	P8	PNP	2	3	20	0	40		50	50	G2	72	
2N291	LP1	PNP	2	4	17	31	4	5	37	50	G1-L		15
2N292	LP1	NPN	2	4	13	50	4		15	50	G1-L	30	3
2N293	LP1	NPN	2	4	13	50	4		15	50	G1-L	35	3
2N296	P3	PNP	2	4	19	0	12		20	50	G2		10
2N297	P3	PNP	2	4	20	0	18		30	31	G2	50	
2N297A	P3	PNP	2	4	19	0	24		30	50	G2		20
2N301	P3	PNP	2	4	12	11	12		66	31	G2	35	
2N301A	P3	PNP	2	4	12	11	12		66	31	G2	35	
2N306	LP1	NPN	2	4	15	11	8		60	50	G1-L		8
2N307	P3	PNP	2	4	18	0	72		90	50	G2		10
2N307A	P3	PNP	2	4	18	0	48		70	50	G2		10
2N310	LP1	PNP	2	4	11	50	4		30	50	G1-L	28	
2N311	LP2	PNP	2	4	13	18	6		36	50	G1-L		12
2N312	LP2	NPN	2	4	13	18	6		36	50	G1-L		12
2N313	LP9	NPN	2	4	13	50	4		15	50	G1-L	30	3
2N314	LP9	NPN	2	4	13	50	4		15	50	G1-L	30	3
2N315	LP2	PNP	2	4	8	50	4		6	50	G1-L		7
2N315A	LP2	PNP	2	4	12	50	4		6	50	G1-L		10
2N316	LP2	PNP	2	4	8	50	4		6	50	G1-L		10
2N316A	LP2	PNP	2	4	9	50	4		4	50	G1-L		10
2N317	LP2	PNP	2	4	8	50	4		6	50	G1-L		10
2N317A	LP2	PNP	2	4	9	50	4		6	50	G1-L		10
2N319	LP1	PNP	2	4	15	50	4		48	50	G1-L		12

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
2N320	LP1	PNP	2	4	15	50	4		48	50	G1-L		20
2N321	LP1	PNP	2	4	15	50	4		48	50	G1-L		27
2N322	LP1	PNP	2	4	13	50	4		48	50	G1-L		12
2N323	LP1	PNP	2	4	13	50	4		48	50	G1-L		20
2N324	LP1	PNP	2	4	13	50	4		48	50	G1-L		27
2N325	P3	PNP	2	4	17	11	12		100	50	G2		8
2N326	P3	PNP	2	4	17	11	12		100	50	G2		8
2N327	LP2	PNP	2	4	15	50	2		2	50	G1-L		4
2N327A	LP2	PNP	2	4	15	50	2	2		50	G1-L	14	
2N328	LP2	PNP	2	4	15	50	2		2	50	G1-L		9
2N328A	LP2	PNP	2	4	15	50	2	2		50	G1-L	18	
2N329	LP2	PNP	2	4	15	50	2		2	50	G1-L		18
2N329A	LP2	PNP	2	4	15	50	2	2		50	G1-L	50	
2N330	LP2	PNP	2	4	15	50	2		2	50	G1-L		4
2N330A	LP2	PNP	2	4	15	50	2	2		50	G1-L	24	
2N332	LP9	NPN	2	4	19	18	4		30	50	G1-L		4
2N333	LP9	NPN	2	4	19	18	4		30	50	G1-L		9
2N334	LP9	NPN	2	4	19	18	4		30	50	G1-L		9
2N335	LP9	NPN	2	4	19	18	4		30	50	G1-L		18
2N336	LP9	NPN	2	4	17	50	4	4		31	G1-L		19
2N337	LP9	NPN	2	4	15	50	2		3	50	G1-L		10
2N338	LP9	NPN	2	4	15	50	2		3	50	G1-L		22
2N339	LP9	NPN	2	4	17	50	2		3	50	G1-L	16	
2N340	LP9	NPN	2	4	17	50	2		3	50	G1-L	16	
2N341	LP9	NPN	2	4	17	50	2		3	50	G1-L	16	
2N342	LP9	NPN	2	4	17	50	2		3	50	G1-L	12	
2N344	LP8	PNP	2	4	8	50	4		9	50	G1-L		5
2N345	LP8	PNP	2	4	8	50	4		9	50	G1-L		12
2N346	LP8	PNP	2	4	8	50	4		9	50	G1-L		5
2N350	P3	PNP	2	4	19	0	14	8		50	G2	30	
2N351	P3	PNP	2	4	18	0	30		45	50	G2	45	
2N352	P6	PNP	2	4	12	11	14		75	50	G2		15
2N353	P6	PNP	2	4	12	11	14		75	50	G2		20
2N354	LP8	PNP	2	4	11	50	2		2	50	G1-L		4
2N355	LP8	PNP	2	4	11	50	2		2	50	G1-L		4
2N356	LP2	NPN	2	4	8	50	4		15	50	G1-L		10
2N356A	LP2	NPN	2	4	9	50	4		15	50	G1-L		10
2N357	LP2	NPN	2	4	8	50	4		15	50	G1-L		10
2N357A	LP2	NPN	2	4	9	50	8		15	50	G1-L		12
2N358	LP2	NPN	2	4	8	50	4		15	50	G1-L		10
2N358A	LP2	NPN	2	4	9	50	8		15	50	G1-L		12
2N359	LP2	PNP	2	4	15	31	4		37	31	G1-L	75	
2N360	LP2	PNP	2	4	15	31	4		38	50	G1-L	100	
2N361	LP2	PNP	2	4	15	31	4		38	50	G1-L	62	
2N362	LP2	PNP	2	4	15	31	4		38	50	G1-L	37	
2N363	LP2	PNP	2	4	15	31	4		38	50	G1-L	62	
2N364	LP1	NPN	2	4	17	50	4		4	50	G1-L	12	
2N365	LP1	NPN	2	4	17	50	4		4	50	G1-L	26	
2N366	LP1	NPN	2	4	17	50	4		4	50	G1-L	65	
2N367	LP1	PNP	2	4	17	31	4		45	50	G1-L		4
2N368	LP1	PNP	2	4	17	31	4		45	50	G1-L		9
2N369	LP1	PNP	2	4	17	31	4		45	50	G1-L		24
2N370	LP10	PNP	2	4	12	50	4		60	50	G1-L	60	

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
2N371	LP10	PNP	2	4	12	50	4		60	50	G1-L	60	
2N372	LP10	PNP	2	4	12	50	4		60	50	G1-L	60	
2N373	LP10	PNP	2	4	12	50	4		24	50	G1-L	60	
2N374	LP10	PNP	2	4	12	50	4		24	50	G1-L	60	
2N375	P3	PNP	2	4	20	0	18		30	50	G2		7
2N376	P3	PNP	2	4	18	0	30		30	50	G2	60	
2N377	LP9	NPN	2	4	15	50	4		60	50	G1-L		10
2N378	P3	PNP	2	4	17	0	12		8	50	G2		10
2N379	P3	PNP	2	4	17	0	12		8	50	G2		10
2N380	P3	PNP	2	4	17	0	12		8	50	G2		15
2N381	LP9	PNP	2	4	17	50	4		30	50	G1-L		10
2N382	LP9	PNP	2	4	17	50	4		30	50	G1-L		20
2N383	LP9	PNP	2	4	17	50	4		30	50	G1-L		30
2N384	LP15	PNP	2	4	12	50	4		48	50	G1-L	60	
2N385	LP9	NPN	2	4	15	31	4		54	50	G1-L		15
2N386	P6	PNP	2	4	20	0	26		50	50	G2		10
2N387	P6	PNP	2	4	20	0	22		50	50	G2		10
2N388	LP9	NPN	2	4	15	50	4		60	50	G1-L		15
2N389	P14	NPN	2	4	20	0	44		100	50	G2		5
2N393	LP8	PNP	2	4	8	50	4		15	50	G1-L		10
2N394	LP16	PNP	2	4	11	50	2		18	50	G1-L		10
2N395	LP16	PNP	2	4	13	50	2		18	50	G1-L		10
2N396	LP16	PNP	2	4	15	50	2		18	50	G1-L		15
2N397	LP16	PNP	2	4	11	50	2		18	50	G1-L		15
2N398	LP2	PNP	2	4	21	18	4		30	50	G1-L		10
2N399	P3	PNP	2	4	17	0	26		20	50	G2	40	
2N400	P3	PNP	2	4	17	0	26		20	31	G2	40	
2N401	P3	PNP	2	4	17	0	26		20	50	G2	40	
2N402	LP2	PNP	2	4	15	50	4		45	50	G1-L	24	
2N403	LP2	PNP	2	4	15	50	4		45	50	G1-L	30	
2N404	LP2-9	PNP	2	4	12	50	4		15	50	G1-L		5
2N405	LP1	PNP	2	4	12	50	4		42	50	G1-L	35	
2N406	LP2	PNP	2	4	12	50	4		42	50	G1-L	35	
2N407	LP1	PNP	2	4	12	50	4		42	50	G1-L	65	
2N408	LP2	PNP	2	4	12	50	4		42	50	G1-L	65	
2N409	LP1	PNP	2	4	12	50	4		30	50	G1-L	45	
2N410	LP2	PNP	2	4	12	50	4		30	50	G1-L	45	
2N411	LP1	PNP	2	4	12	50	4		30	50	G1-L	75	
2N412	LP2	PNP	2	4	12	50	4		30	50	G1-L	75	
2N413	LP2	PNP	2	4	12	50	2		15	50	G1-L	25	
2N414	LP2	PNP	2	4	12	50	2		15	50	G1-L	30	
2N416	LP2	PNP	2	4	12	50	2		15	50	G1-L	80	
2N417	LP2	PNP	2	4	12	50	2		15	31	G1-L	55	
2N418	P3	PNP	2	4	20	0	12		20	50	G2		20
2N419	P3	PNP	2	4	17	0	26		20	50	G2	60	
2N420	P3	PNP	2	4	19	0	14		15	50	G2		15
2N420A	P3	PNP	2	4	20	0	12		20	50	G2		20
2N421	P3	PNP	2	4	19	0	14		15	50	G2	50	
2N422	LP2	PNP	2	4	15	50	4		45	50	G1-L	50	
2N424	P14	NPN	2	4	20	0	44		100	50	G2		4
2N425	LP2	PNP	2	4	2	50	4		12	50	G1-L		10
2N426	LP2	PNP	2	4	17	31	4		38	50	G1-L		5
2N427	LP2	PNP	2	4	17	31	4		38	50	G1-L		7

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	IcBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
2N428	LP2	PNP	2	4	17	31	4		38	50	G1-L		10
2N438	LP2	NPN	2	4	17	50	4		30	50	G1-L		10
2N438A	LP2	NPN	2	4	17	50	4		30	50	G1-L		10
2N439	LP2	NPN	2	4	17	50	4		30	50	G1-L		15
2N439A	LP2	NPN	2	4	17	50	4		30	50	G1-L		15
2N440	LP2	NPN	2	4	17	50	4		30	50	G1-L		20
2N440A	LP2	NPN	2	4	17	50	4		30	50	G1-L		20
2N441	P8	PNP	2	3	19	0	56		100	50	G2	33	
2N442	P8	PNP	2	3	19	0	48		100	50	G2	33	
2N443	P8	PNP	2	3	20	0	44		100	50	G2	33	
2N444	LP2	NPN	2	4	11	50	4		18	50	G1-L		5
2N444A	LP2	NPN	2	4	8	50	4		12	50	G1-L		10
2N445	LP2	NPN	2	4	11	50	4		18	50	G1-L		10
2N445A	LP2	NPN	2	4	8	50	4		12	50	G1-L		20
2N446	LP2	NPN	2	4	11	50	4		18	50	G1-L		15
2N446A	LP2	NPN	2	4	8	50	4		12	50	G1-L		30
2N447	LP2	NPN	2	4	11	50	4		18	50	G1-L		25
2N447A	LP2	NPN	2	4	8	50	4		12	31	G1-L		20
2N450	LP16	PNP	2	4	9	50	4		18	50	G1-L		15
2N456	P3	PNP	2	4	19	0	14		15	50	G2		15
2N457	P3	PNP	2	4	20	0	12		20	50	G2		15
2N459	P3	PNP	2	4	17	0	12		8	50	G2	40	
2N460	LP9	PNP	2	4	19	50	4		45	50	G1-L		9
2N461	LP9	PNP	2	4	19	50	4		45	50	G1-L		16
2N464	LP2	PNP	2	4	15	50	2		45	50	G1-L		7
2N465	LP2	PNP	2	4	15	50	2		45	50	G1-L		13
2N466	LP2	PNP	2	4	15	50	2		45	50	G1-L		28
2N467	LP2	PNP	2	4	15	50	2		45	31	G1-L		28
2N471A	LP9	NPN	2	4	17	50	2		2	50	G1-L		5
2N473	LP9	NPN	2	4	13	50	2		2	50	G1-L		10
2N474	LP9	NPN	2	4	17	50	2		2	50	G1-L		10
2N474A	LP9	NPN	2	4	17	50	2		2	50	G1-L		10
2N475	LP9	NPN	2	4	19	50	2		2	50	G1-L		10
2N476	LP9	NPN	2	4	13	50	2		2	50	G1-L		15
2N477	LP9	NPN	2	4	17	50	2		2	50	G1-L		15
2N478	LP9	NPN	2	4	13	50	2		2	50	G1-L		20
2N479	LP9	NPN	2	4	17	50	2		2	50	G1-L		20
2N479A	LP9	NPN	2	4	17	50	2		2	50	G1-L		20
2N480	LP9	NPN	2	4	19	50	2		2	50	G1-L		20
2N495	LP2	PNP	2	4	11	50	2	2		50	G1-L	18	
2N496	LP2	PNP	2	4	11	50	2	2		50	G1-L	18	
2N497	LP9	NPN	2	4	19	18	4		60	50	G2		6
2N498	LP9	NPN	2	4	21	18	4		60	50	G2		6
2N499	LP2	PNP	2	4	13	50	4		45	50	G1-L		3
2N501	LP2	PNP	2	4	9	50	4		15	50	G1-L		10
2N501A	LP2	PNP	2	4	13	31	4		38	50	G1-L		5
2N502A	LP2	PNP	2	4	11	50	4		15	50	G1-L		5
2N503	LP2	PNP	2	4	11	50	4		15	50	G1-L		5
2N504	LP2	PNP	2	4	11	50	4		30	50	G1-L		8
2N508	LP1	PNP	2	4	13	50	4		48	31	G1-L	62	
2N515	LP1	NPN	2	4	14	31	16	25		50	G1-L	7	
2N516	LP1	NPN	2	4	14	31	16	25		50	G1-L	7	
2N517	LP1	NPN	2	4	14	31	16	25		50	G1-L	7	

Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	IcBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
2N518	LP16	PNP	2	4	12	50	2						
2N519	LP2	PNP	2	4	8	50	4		18	50	G1-L		30
2N519A	LP2	PNP	2	4	8	50	4		6	50	G1-L		8
2N520	LP2	PNP	2	4	8	50	4		6	50	G1-L		10
2N520A	LP2	PNP	2	4	8	50	4		6	50	G1-L		10
2N521	LP2	PNP	2	4	8	50	4		6	50	G1-L		20
2N521A	LP2	PNP	2	4	8	50	4		6	50	G1-L		18
2N522	LP2	PNP	2	4	8	50	4		6	50	G1-L		30
2N522A	LP2	PNP	2	4	8	50	4		6	50	G1-L		30
2N523	LP2	PNP	2	4	8	50	4		6	31	G1-L		20
2N523A	LP2	PNP	2	4	8	50	4		6	31	G1-L		20
2N524	LP16	PNP	2	4	8	50	4		12	31	G1-L		25
2N525	LP16	PNP	2	4	18	50	2		30	50	G1-L		6
2N526	LP16	PNP	2	4	18	50	2		30	50	G1-L		15
2N527	LP16	PNP	2	4	18	50	2		30	50	G1-L		24
2N529	LP2	*	2	4	8	50	4		30	50	G1-L		32
			2	4	8	50	4		15	50	G1-L		8
*Matched Pair—one PNP (identified by red dot), one NPN (identified by black dot)													
2N530	LP2	*	2	4	8	50	4		15	50	G1-L		10
*Matched Pair—one PNP (identified by red dot), one NPN (identified by black dot)													
2N531	LP2	*	2	4	8	50	4		15	50	G1-L		12
*Matched Pair—one PNP (identified by red dot), one NPN (identified by black dot)													
2N532	LP2	*	2	4	8	50	4		15	50	G1-L		15
*Matched Pair—one PNP (identified by red dot), one NPN (identified by black dot)													
2N533	LP2	*	2	4	8	50	4		15	50	G1-L		17
*Matched Pair—one PNP (identified by red dot), one NPN (identified by black dot)													
2N534	LP8	PNP	2	4	19	50	4		45	50	G1-L		18
2N535	LP8	PNP	2	4	12	50	4		30	50	G1-L		18
2N535A	LP8	PNP	2	4	12	50	4		30	50	G1-L		18
2N535B	LP8	PNP	2	4	12	50	4		30	50	G1-L		18
2N536	LP8	PNP	2	4	12	50	4		30	50	G1-L		18
2N538	P1	PNP	2	4	20	0	14		20	50	G2		25
2N538A	P1	PNP	2	4	20	0	14		20	50	G2		10
2N539	P1	PNP	2	4	20	0	14		20	50	G2		10
2N539A	P1	PNP	2	4	20	0	14		20	50	G2		15
2N540	P1	PNP	2	4	20	0	14		20	50	G2		15
2N540A	P1	PNP	2	4	20	0	14		20	50	G2		22
2N543	LP2	NPN	2	4	19	50	2		2	31	G1-L		20
2N544	LP10	PNP	2	4	12	50	4		12	50	G1-L		20
2N545	LP9	NPN	2	4	20	50	4		45	50	G1-L	60	7
2N546	LP9	NPN	2	4	17	50	4		45	50	G2		7
2N547	LP9	NPN	2	4	20	50	4		45	50	G2		10
2N548	LP9	NPN	2	4	17	50	4		45	50	G2		10
2N549	LP9	NPN	2	4	20	50	4		9	50	G2		10
2N550	LP9	NPN	2	4	17	50	4		9	50	G2		10
2N551	LP9	NPN	2	4	20	50	4		45	50	G2		10
2N552	LP9	NPN	2	4	17	50	4		45	50	G2		10
2N553	P3	PNP	2	4	20	0	12		20	50	G2		10
2N554	P3	PNP	2	4	17	0	44		50	50	G2		15

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
2N555	P3	PNP	2	4	17	0	44			50	G2		15
2N561	P3	PNP	2	4	18	31	12	50	50	50	G2	26	
2N564	LP2	PNP	2	4	11	50	4	15	50	50	G1-L		5
2N566	LP2	PNP	2	4	11	50	4	15	50	50	G1-L		15
2N568	LP2	PNP	2	4	11	50	4	15	50	50	G1-L		25
2N570	LP2	PNP	2	4	11	50	4	15	50	50	G1-L		35
2N572	LP2	PNP	2	4	11	50	4	15	31	50	G1-L		25
2N574	P1	PNP	2	4	20	0	60	70	50	50	G2		4
2N574A	P1	PNP	2	4	20	0	60	70	50	50	G2		4
2N575	P1	PNP	2	4	20	0	60	70	50	50	G2		9
2N575A	P1	PNP	2	4	20	0	60	70	50	50	G2		9
2N576	LP9	NPN	2	4	15	50	4	10	50	50	G1-L	40	
2N578	LP2	PNP	2	4	12	50	4	15	50	50	G1-L		5
2N579	LP2	PNP	2	4	12	50	4	15	50	50	G1-L		10
2N580	LP2	PNP	2	4	12	50	4	15	50	50	G1-L		15
2N581	LP2	PNP	2	4	9	50	4	18	50	50	G1-L		10
2N582	LP2	PNP	2	4	12	50	4	15	50	50	G1-L		20
2N583	LP3	PNP	2	4	9	50	4	18	50	50	G1-L		10
2N584	LP3	PNP	2	4	12	50	4	15	50	50	G1-L		10
2N585	LP2	NPN	2	4	12	50	4	24	50	50	G1-L		10
2N586	LP1	PNP	2	4	17	50	4	48	50	50	G1-L		15
2N587	LP9	NPN	2	4	19	50	4	25	50	50	G1-L		
2N591	LP3	PNP	2	4	2	50	4	21	50	50	G1-L	70	
2N592	LP2	PNP	2	4	8	50	4	15	50	50	G1-L		10
"	LP2	PNP	2	1	8	50	4	15	50	50	G1-L		10
2N593	LP2	PNP	2	4	8	50	4	15	50	50	G1-L		8
"	LP2	PNP	2	1	8	50	4	15	50	50	G1-L		8
2N594	LP2	NPN	2	4	8	50	4	15	50	50	G1-L		10
"	LP2	NPN	2	1	8	50	4	15	50	50	G1-L		10
2N595	LP2	NPN	2	4	8	50	4	15	50	50	G1-L		8
"	LP2	NPN	2	1	8	50	4	15	50	50	G1-L		8
2N596	LP2	NPN	2	4	8	50	4	15	50	50	G1-L		10
"	LP2	NPN	2	1	8	50	4	15	50	50	G1-L		10
2N597	LP2*	PNP	2	4	19	31	4	38	50	50	G1-L		20
*Lead No. 4 connected to case, keep transistor from touching instrument panel.													
2N598	LP2*	PNP	2	4	17	31	4	38	50	50	G1-L		25
*Lead No. 4 connected to case, keep transistor from touching instrument panel.													
2N599	LP2*	PNP	2	4	17	31	4	38	31	50	G1-L		17
*Lead No. 4 connected to case, keep transistor from touching instrument panel.													
2N601	LP2*	PNP	2	4	17	31	4	38	50	50	G1-L		35
*Lead No. 4 connected to case, keep transistor from touching instrument panel.													
2N602	LP2	PNP	2	4	11	50	4	24	50	50	G1-L		10
2N603	LP2	PNP	2	4	11	50	4	24	50	50	G1-L		15
2N604	LP2	PNP	2	4	11	50	4	24	50	50	G1-L		20
2N605	LP2	PNP	2	4	12	50	4	30	50	50	G1-L	40	
2N606	LP2	PNP	2	4	12	50	4	30	50	50	G1-L	60	
2N607	LP2	PNP	2	4	12	50	4	30	31	50	G1-L	40	
2N608	LP2	PNP	2	4	12	50	4	30	31	50	G1-L	60	
2N609	LP2	PNP	2	4	15	31	4	38	31	50	G1-L	35	

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	ICBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
2N610	LP2	PNP	2	4	15	31	4		38	31	G1-L	32	
2N611	LP2	PNP	2	4	15	31	4		38	50	G1-L	45	
2N612	LP2	PNP	2	4	15	31	4		38	50	G1-L	40	
2N613	LP2	PNP	2	4	15	31	4		38	50	G1-L	60	
2N618	P3	PNP	2	4	20	0	18		30	50	G2		30
2N619	LP2	NPN	2	4	15	50	2	2		50	G1-L	14	
2N620	LP2	NPN	2	4	15	50	2	2		50	G1-L	25	
2N623	LP11	PNP	2	4	15	50	4			50	G1-L		5
2N625	LP2	NPN	2	4	19	50	8	50	30	50	G2	30	
2N627	P3	PNP	2	4	19	0	24		30	50	G2		5
2N628	P3	PNP	2	4	20	0	18		30	50	G2		5
2N629	P3	PNP	2	4	20	0	14		30	50	G2		5
2N630	P3	PNP	2	4	21	0	12		30	50	G2		5
2N637	P3	PNP	2	4	17	0	14		10	50	G2		15
2N637A	P3	PNP	2	4	20	0	26		50	50	G2		15
2N637B	P3	PNP	2	4	20	0	26		50	50	G2		15
2N638	P3	PNP	2	4	17	0	14		10	50	G2		10
2N638A	P3	PNP	2	4	20	0	26		50	50	G2		10
2N638B	P3	PNP	2	4	20	0	26		50	50	G2		10
2N639	P3	PNP	2	4	17	0	14		10	50	G2		10
2N639A	P3	PNP	2	4	20	0	26		50	50	G2		7
2N639B	P3	PNP	2	4	20	0	26		50	50	G2		7
2N640	LP10	PNP	2	4	12	50	6		15	50	G1-L	50	
2N641	LP10	PNP	2	4	12	50	4		21	50	G1-L	50	
2N642	LP10	PNP	2	4	11	50	4		21	50	G1-L	50	
2N643	LP2	PNP	2	4	10	50	6	3		50	G1-L	45	
2N644	LP2	PNP	2	4	10	50	6	3		50	G1-L	45	
2N645	LP2	PNP	2	4	10	50	6	3		50	G1-L	45	
2N647	LP3	NPN	2	4	17	50	4		42	31	G1-L	35	
2N649	LP2	NPN	2	4	12	50	4		42	31	G1-L	31	
2N650	LP2	PNP	2	4	17	50	4		45	50	G1-L	40	
2N651	LP2	PNP	2	4	17	50	4		45	50	G1-L		25
2N652	LP2	PNP	2	4	17	50	4		45	31	G1-L		25
2N653	LP2	PNP	2	4	17	50	4		45	50	G1-L		12
2N654	LP2	PNP	2	4	17	50	4		45	50	G1-L		25
2N655	LP2	PNP	2	4	17	50	4		45	31	G1-L		25
2N656	LP9*	NPN	2	4	17	50	4		30	50	G2	80	15
*Lead No. 2 connected to case, keep transistor from touching instrument panel.													
2N657	LP9*	NPN	2	4	17	50	4		30	50	G2		15
*Lead No. 2 connected to case, keep transistor from touching instrument panel.													
2N670	LP2*	PNP	2	4	12	18	6		45	50	G1-L		20
*Lead No. 1 connected to case, keep transistor from touching instrument panel.													
2N671	LP2*	PNP	2	4	12	18	6		45	50	G2		20
*Lead No. 1 connected to case, keep transistor from touching instrument panel.													
2N677	P3	PNP	2	4	13	0	38		20	50	G2		10
2N677A	P3	PNP	2	4	17	0	26		20	50	G2		10
2N677B	P3	PNP	2	4	19	0	26		50	50	G2		10
2N677C	P3	PNP	2	4	19	0	26		50	50	G2		10
2N678	P3	PNP	2	4	13	0	38		20	50	G2		25

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
2N678A	P3	PNP	2	4	17	0	38			50	G2		25
2N678B	P3	PNP	2	4	19	0	26			50	G2		25
2N678C	P3	PNP	2	4	19	0	26			50	G2		25
2N695	LP11	PNP	2	4	9	50	4			30	G1-L	30	
2N700	LP11*	PNP	2	4	12	50	4			30	G1-L	10	
*Lead No. 3 is shield.													
2N1008	LP2	PNP	2	4	11	50	6	5		50	G1-L		20
2N1008A	LP2	PNP	2	4	17	50	4	5		50	G1-L		20
2N1008B	LP2	PNP	2	4	19	50	4	7		50	G1-L		20
2N1010	LP3	NPN	2	4	11	50	4		30	50	G1-L	35	
2N1014	P3	PNP	2	4	17	11	44	50		50	G2	26	
2N1024	LP9	PNP	2	4	13	50	2		2	50	G1-L		4
2N1025	LP9	PNP	2	4	18	50	2		2	50	G1-L		4
2N1026	LP9	PNP	2	4	18	50	2		2	50	G1-L		9
2N1026A	LP9	PNP	2	4	18	50	4		2	50	G1-L		18
2N1027	LP9	PNP	2	4	13	50	2		2	50	G1-L		9
2N1028	LP9	PNP	2	4	11	50	2		2	50	G1-L		4
2N1029	P3	PNP	2	4	13	0	38			20	G2		10
2N1029A	P3	PNP	2	4	17	0	26			20	G2		10
2N1029B	P3	PNP	2	4	19	0	26			50	G2		10
2N1029C	P3	PNP	2	4	19	0	26			50	G2		10
2N1030	P3	PNP	2	4	13	0	38			20	G2		25
2N1030A	P3	PNP	2	4	17	0	26			20	G2		25
2N1030B	P3	PNP	2	4	19	0	26			50	G2		25
2N1030C	P3	PNP	2	4	19	0	26			50	G2		25
2N1031	P3	PNP	2	4	13	0	38			20	G2		10
2N1031A	P3	PNP	2	4	17	0	26			20	G2		10
2N1031B	P3	PNP	2	4	19	0	26			50	G2		10
2N1031C	P3	PNP	2	4	19	0	26			50	G2		10
2N1032	P3	PNP	2	4	13	0	38			20	G2		25
2N1032A	P3	PNP	2	4	17	0	26			20	G2		25
2N1032B	P3	PNP	2	4	19	0	26			50	G2		25
2N1032C	P3	PNP	2	4	19	0	26			50	G2		25
2N1067	P2	NPN	2	4	19	50	4	15		50	G2	35	
2N1068	P2	NPN	2	4	19	50	4	15		50	G2	38	
2N1069	P3	NPN	2	4	19	50	4	25		50	G2	20	
2N1070	P3	NPN	2	4	19	50	6	25		50	G2	20	
2N1073	P3	PNP	2	4	17	0	26			20	G2		10
2N1073A	P3	PNP	2	4	19	0	26			50	G2		10
2N1073B	P3	PNP	2	4	21	0	30			26	G2		10
2N1090	LP2	NPN	2	4	12	50	4		4	50	G1-L		15
2N1091	LP2	NPN	2	4	12	50	4		4	50	G1-L		15
2N1092	P2	NPN	2	4	19	50	4		15	50	G2	40	
2N1099	P8	PNP	2	3	20	0	30			80	G2		17
2N1100	P8	PNP	2	3	21	0	26			80	G2		12
2N1124	LP2*	PNP	2	4	19	18	4			45	G1-L		20

*Lead No. 1 connected to case, keep transistor from touching instrument panel.

2N1125	LP2*	PNP	2	4	19	18	4			45	50	G1-L		25
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*Lead No. 1 connected to case, keep transistor from touching instrument panel.

2N1126	LP2*	PNP	2	4	19	18	4			45	50	G2		20
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*Lead No. 1 connected to case, keep transistor from touching panel.

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	ICBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
2N1127	LP2*	PNP	2	4	19	18	4		45	50	G2		25
*Lead No. 1 connected to case, keep transistor from touching panel.													
2N1128	LP2-3	PNP	2	4	11	50	4		60	50	G1-L		35
2N1129	LP2	PNP	2	4	17	31	4		38	31	G1-L		25
2N1146	P3	PNP	2	4	15	0	48		40	50	G2	30	
2N1146A	P3	PNP	2	4	17	0	38		40	50	G2		30
2N1146B	P3	PNP	2	4	19	0	30		40	50	G2		30
2N1146C	P3	PNP	2	4	19	0	26		40	50	G2		30
2N1147	P3	PNP	2	4	15	0	48		40	50	G2		30
2N1147A	P3	PNP	2	4	17	0	38		40	50	G2		30
2N1147B	P3	PNP	2	4	19	0	30		40	50	G2		30
2N1147C	P3	PNP	2	4	19	0	26		40	50	G2		30
2N1157	P1	PNP	2	4	19	0	12		105	50	G2		20
2N1157A	P1	PNP	2	4	19	0	12		105	50	G2		20
2N1191	LP2	PNP	2	4	17	50	4		48	50	G1-L		15
2N1192	LP2	PNP	2	4	17	50	4		45	50	G1-L		25
2N1193	LP2	PNP	2	4	17	50	4		45	31	G1-L		25
2N1202	P1	PNP	2	4	20	0	10		20	50	G2		20
2N1203	P1	PNP	2	4	21	0	10		20	50	G2		12
2N1219	LP2	PNP	2	4	11	50	2		2	50	G1-L		9
2N1220	LP2	PNP	2	4	11	50	2		2	50	G1-L		5
2N1221	LP2	PNP	2	4	11	50	2		2	50	G1-L		9
2N1222	LP2	PNP	2	4	11	50	2		2	50	G1-L		5
2N1223	LP2	PNP	2	4	11	50	2		2	50	G1-L		3
2N1261	P1	PNP	2	4	17	0	14		15	50	G2		10
2N1262	P1	PNP	2	4	17	0	14		15	50	G2		15
2N1263	P1	PNP	2	4	17	0	14		15	50	G2		23
2N1300	LP2	PNP	2	4	9	50	4		9	50	G1-L		15
2N1301	LP2	PNP	2	4	9	50	4		9	50	G1-L		15
2-OC-16*	P10	PNP	2	4	13	18	8		60	50	G2		10
*Matched pair of OC-16's.													
2-OC-72*	LP1	PNP	2	4	11	50	4		30	50	G1-L		12
*Matched pair of OC-72's.													
3N29	LP11	NPN	2-3*	4	10	31	6		38	31	G1-L	50	
*Tetrode Transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													
3N30	LP11	NPN	2-3*	4	10	31	6		38	31	G1-L	50	
*Tetrode Transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													
3N31	LP11	NPN	2-3*	4	10	31	6		38	31	G1-L	50	
*Tetrode Transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													
3N35	LP11	NPN	2-3*	4	15	50	2		2	50	G1-L	8	
*Tetrode Transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													
3N36	LP11	NPN	2-3*	4	10	50	8		30	50	G1-L		4
*Tetrode Transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													
3N37	LP11	NPN	2-3*	4	10	50	8		30	50	G1-L		6
*Tetrode Transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
4JD1A17	LP1	PNP	2	4	19	50	4		48	50	G1-L		10
4JD1B3	LP1	PNP	2	4	17	50	4		60	50	G1-L		7
4JD1B4	LP1	PNP	2	4	17	50	4		60	50	G1-L		10
4JD4A2	LP9	NPN	2	4	8	50	2		2	50	G1-L		4
4JD4A3	LP9	NPN	2	4	8	50	2		2	50	G1-L	30	
4JD4A4	LP9	NPN	2	4	8	50	2		2	50	G1-L		4
4JD4A5	LP9	NPN	2	4	8	50	2		2	50	G1-L		9
200A	LP1	NPN	2	4	17	50	4		30	50	G1-L		4
201A	LP1	NPN	2	4	17	50	4		30	50	G1-L		9
202A	LP1	NPN	2	4	17	50	4		30	50	G1-L		24
300	LP1	PNP	2	4	17	31	4		45	50	G1-L		4
301	LP1	PNP	2	4	17	31	4		45	50	G1-L		9
302	LP1	PNP	2	4	17	31	4		45	50	G1-L		24
310	LP1	PNP	2	4	15	50	4		60	50	G1-L	40	
355	P8	PNP	2	3	18	0	12		10	50	G2		20
440C-C	P5	PNP	2	3	17	0	12		8	50	G2		11
440C-E	P5	PNP	2	3	17	0	12		8	50	G2		11
441C	P5	PNP	2	3	17	0	12		8	50	G2		20
442C	P5	PNP	2	3	13	18	6		60	50	G2		15
442C-A	P5	PNP	2	3	13	18	6		60	50	G2		15
442C-D	P5	PNP	2	3	13	18	6		60	50	G2		15
903	LP1	NPN	2	4	17	50	4		30	50	G1-L		4
904	LP1	NPN	2	4	17	50	4		30	50	G1-L		9
904A	LP1	NPN	2	4	17	50	4		30	50	G1-L		9
905	LP1	NPN	2	4	17	50	4		30	50	G1-L		18
910	LP1	NPN	2	4	17	50	4		30	50	G1-L		38
951	LP1	NPN	2	4	19	50	2		15	50	G1-L		4
952	LP1	NPN	2	4	20	50	2		18	50	G1-L		4
953	LP1	NPN	2	4	21	50	2		24	50	G1-L		4
970	LP12	NPN	2	3	21	11	4		30	50	G1-L	20	3
AR-5	P3	PNP	2	4	17	0	20		15	50	G2		12
B-112	P3	PNP	2	4	17	0	14		10	31	G2		20
B-134	P3	PNP	2	4	17	0	26		20	50	G2		7
B-134A	P3	PNP	2	4	19	0	26		50	50	G2		7
B-134B	P3	PNP	2	4	19	0	26		50	50	G2		7
B-1058	LP2	PNP	2	4	11	31	4		38	50	G1-L		10
B-1059	LP2	PNP	2	4	11	31	4		38	50	G1-L		20
CK-721	LP5	PNP	2	4	9	50	8	6		50	G1-L	45	
CK-722	LP5	PNP	2	4	9	50	8	6		50	G1-L	22	
CK-725	LP5	PNP	2	4	9	50	8	6		31	G1-L	45	
CK-727	LP5	PNP	2	4	5	50	14	6		50	G1-L	25	
CK-751	LP5	PNP	2	4	15	31	4		38	50	G1-L		22
CK-754	LP5	PNP	2	4	15	50	4		30	31	G1-L		50
CK-759	LP1	PNP	2	4	12	50	4		15	50	G1-L		8
CK-760	LP1	PNP	2	4	12	50	4		15	50	G1-L		8
CK-761	LP1	PNP	2	4	12	50	4		15	50	G1-L		10
CK-762	LP1	PNP	2	4	12	50	4		15	50	G1-L		10
CK-766	LP1	PNP	2	4	12	50	4		15	50	G1-L	45	
CK-766A	LP1	PNP	2	4	12	50	4		15	50	G1-L	45	
CK-768	LP1	PNP	2	4	12	50	4		15	50	G1-L		6
CK-790	LP1	PNP	2	4	15	50	2		2	50	G1-L		4
CK-791	LP1	PNP	2	4	15	50	2		2	50	G1-L		9
CK-793	LP1	PNP	2	4	15	50	2		2	50	G1-L		4

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	ICBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
CQ-1	LP1	PNP	2	4	9	50	4						
CTP-1002	P5	PNP	2	3	19	18	10	20	60	50	G1-L		4
CTP-1003	P5	PNP	2	3	19	18	10	20		50	G2	35	
CTP-1004	P5	PNP	2	3	19	18	12	20		50	G2	15	
CTP-1005	P5	PNP	2	3	19	18	12	20		50	G2	15	
CTP-1006	P5	PNP	2	3	19	18	12	20		50	G2	25	
CTP-1032	LP1	PNP	2	4	9	50	4	20		50	G2	35	
CTP-1033	LP1	PNP	2	4	9	50	4		30	50	G1-L		4
CTP-1034	LP1	PNP	2	4	9	50	4		30	50	G1-L		8
CTP-1035	LP1	PNP	2	4	9	50	4		30	50	G1-L		16
CTP-1036	LP1	PNP	2	4	9	50	4		30	50	G1-L		25
CTP-1104	P3	PNP	2	4	19	0	18	20		50	G1-L		35
CTP-1108	P3	PNP	2	4	15	0	30	20		50	G2	30	
CTP-1109	P3	PNP	2	4	15	0	30	20		50	G2	30	
CTP-1111	P3	PNP	2	4	20	0	12	20		50	G2	60	
CTP-1320	LP6	PNP	2	4	9	50	4		30	50	G2	30	
CTP-1330	LP6	PNP	2	4	9	50	4		30	50	G1-L		4
CTP-1340	LP6	PNP	2	4	9	50	4		30	50	G1-L		8
CTP-1350	LP6	PNP	2	4	9	50	4		30	50	G1-L		16
CTP-1360	LP6	PNP	2	4	9	50	4		30	50	G1-L		25
DT-80	P8	PNP	2	3	20	0	4		30	50	G1-L		35
DT-100	P8	PNP	2	3	21	0	30	20		50	G2		17
GFT-20	LP7	PNP	2	4	6	50	4		100	50	G2	39	
GT-14	LP1	PNP	2	4	13	50	4		30	50	G1-L		8
GT-14H	LP1-2	PNP	2	4	9	50	4		45	50	G1-L		10
GT-20	LP1	PNP	2	4	13	50	4		30	50	G1-L		10
GT-20H	LP1	PNP	2	4	9	50	4		45	50	G1-L		17
GT-34	LP1	PNP	2	4	9	50	4		30	50	G1-L		18
GT-34HV	LP1	PNP	2	4	13	50	4		45	50	G1-L		5
GT-34N	LP1	PNP	2	4	19	31	4		38	50	G1-L		5
GT-34S	LP1-2	PNP	2	4	21	0	22	7		50	G1-L	18	
GT-35	LP1-2	PNP	2	4	13	50	4		45	50	G1-L		5
GT-74	LP1-2	NPN	2	4	11	50	4		38	50	G1-L	40	
GT-75	LP1-2	PNP	2	4	11	50	6	6		50	G1-L	75	
GT-81	LP1	PNP	2	4	11	50	6	6		31	G1-L	75	
GT-81H	LP1-2	PNP	2	4	13	50	4		45	50	G1-L		25
GT-81HS	LP1	PNP	2	4	9	50	4		30	50	G1-L		20
GT-82	LP1-2	PNP	2	4	8	50	4		30	31	G1-L		20
GT-83	LP1-2	PNP	2	4	11	50	6	6		31	G1-L	75	
GT-87	LP1	PNP	2	4	13	50	4		45	50	G1-L		18
GT-88	LP1	PNP	2	4	17	31	4		38	50	G1-L		10
GT-109	LP1	PNP	2	4	17	18	4		30	50	G1-L		25
GT-122	LP1	PNP	2	4	8	50	4		30	31	G1-L		20
GT-123	LP2	PNP	2	4	17	18	4		30	50	G1-L		25
GT-153	LP2	PNP	2	4	15	50	4		18	50	G1-L		15
GT-167	LP2	NPN	2	4	15	50	4		15	50	G1-L		10
GT-222	LP1	PNP	2	4	13	50	4		30	50	G1-L		12
GT-229	LP2	NPN	2	4	8	50	6		60	50	G1-L	20	
GT-269	LP2	PNP	2	4	11	50	4		30	50	G1-L		5
GT-759	LP1-2	PNP	2	4	12	50	4		12	50	G1-L		14
GT-759R	LP1-2	PNP	2	4	8	50	4		30	50	G1-L		5
GT-760	LP1	PNP	2	4	11	50	6	6		50	G1-L	25	
GT-760R	LP1-2	PNP	2	4	8	50	4		30	50	G1-L		10
			2	4	11	50	6	6		50	G1-L	40	

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	ICBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
GT-761	LP1	PNP	2	4	8	50	4		30	50	G1-L		25
GT-761R	LP1-2	PNP	2	4	11	50	6	6	31	50	G1-L	35	
GT-762	LP1	PNP	2	4	8	50	4		30	50	G1-L		20
GT-762R	LP1-2	PNP	2	4	11	50	6	6	31	50	G1-L	60	
GT-763	LP1	PNP	2	4	8	50	4		30	50	G1-L		25
GT-764	LP1-2	PNP	2	4	13	50	4		15	31	G1-L	100	
GT-792	LP2	NPN	2	4	8	50	8	6	50	50	G1-L		19
GT-792R	LP2	NPN	2	4	11	50	6	6	50	50	G1-L		10
GT-903	LP2	NPN	2	4	15	31	4		38	50	G1-L		18
GT-904	LP2	NPN	2	4	15	31	4		38	50	G1-L		10
GT-905	LP2	NPN	2	4	15	31	4		38	50	G1-L		5
GT-905R	LP2	NPN	2	4	11	50	6	6	50	50	G1-L		10
GT-947	LP2	NPN	2	4	11	31	4		38	50	G1-L		20
GT-948	LP2	NPN	2	4	15	50	4		60	50	G1-L		15
GT-949	LP1	NPN	2	4	17	31	4		38	50	G1-L		15
GT-949R	LP2	NPN	2	4	11	50	6	6	31	50	G1-L		10
GT-1200	LP2	NPN	2	4	9	50	4		21	50	G1-L		10
GT-SMP-A	LP2	*	2	4	8	50	4		15	50	G1-L		7
*Matched pair—one PNP (identified by red dot), one NPN (identified by black dot).													
GT-SMP-B	LP2	*	2	4	8	50	4		15	50	G1-L		10
*Matched pair—one PNP (identified by red dot), one NPN (identified by black dot).													
GT-SMP-C	LP2	*	2	4	8	50	4		15	50	G1-L		12
*Matched pair—one PNP (identified by red dot), one NPN (identified by black dot).													
GT-SMP-D	LP2	*	2	4	8	50	4		15	50	G1-L		15
*Matched pair—one PNP (identified by red dot), one NPN (identified by black dot).													
GT-SMP-E	LP2	*	2	4	8	50	4		15	50	G1-L		17
*Matched pair—one PNP (identified by red dot), one NPN (identified by black dot).													
H-3	P1-13	PNP	2	3-4	19	0	10		10	50	G2		5
H-3-A	P1-13	PNP	2	3-4	19	0	10		10	50	G2		5
H-4	P1-13	PNP	2	3-4	19	0	10		10	50	G2		5
H-4-A	P1-13	PNP	2	3-4	19	0	10		10	50	G2		5
H-5	P1	PNP	2	4	19	0	18		30	50	G2		10
H-6	P1	PNP	2	4	19	0	18		30	50	G2		15
H-7	P1	PNP	2	4	19	0	18		30	50	G2		22
H-10	P1†	PNP	2	4	20	0	34		70	50	G2		5
†Terminals stamped on transistor—connect E to 1, B to 2, C to 4.													
H-45	P1	PNP	2	4	19	0	12		30	50	G2		7
H-200E	*P1	PNP	2	4	19	0	12		20	50	G2	40	
*Terminals stamped on transistor—connect E to 1, B to 2, C to 4.													
HA-5001	LP13	NPN	2	3	8	50	4		30	50	G1-L		19
HA-5002	LP13	NPN	2	3	8	50	6		60	50	G1-L		10
HA-5003	LP13	NPN	2	3	8	50	4		45	50	G1-L		19
HA-5005	LP13	NPN	2	3	8	31	6		45	50	G1-L	100	3
HA-5009	LP13	NPN	2	3	8	31	6		45	50	G1-L	100	3
HA-5011	LP13	NPN	2	3	8	50	6		60	50	G1-L		19
HA-5012	LP13	NPN	2	3	8	50	6		60	50	G1-L		9

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	ICBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
HA-5014	LP13	NPN	2	3	8	50	6		60	50	G1-L		19
HA-5016	LP13	NPN	2	3	8	50	4		15	50	G1-L		9
HA-5020	LP13	NPN	2	3	8	50	4		30	50	G1-L		15
HA-5021	LP13	NPN	2	3	8	50	4		30	50	G1-L		15
HA-5022	LP13	NPN	2	3	8	50	4		21	50	G1-L		15
HA-7501	LP13	PNP	2	3	8	50	2		2	50	G1-L	7	2
HA-7502	LP13	PNP	2	3	8	50	2		2	50	G1-L		6
HD-337	P2	PNP	2	4	18	0	18		15	50	G2		7
HD-369	P2	PNP	2	4	19	0	12		15	50	G2		6
HF-1	LP1	PNP	2	4	9	50	4		15	50	G1-L		19
HF-2	LP1	PNP	2	4	9	50	4		15	50	G1-L		19
IF-1	LP1	PNP	2	4	9	50	4		15	50	G1-L		32
IF-2	LP1	PNP	2	4	9	50	4		30	50	G1-L		16
IF-3	LP1	PNP	2	4	9	50	4		30	50	G1-L		12
J-1	LP1	PNP	2	4	9	50	4		45	50	G1-L		16
J-2	LP1	PNP	2	4	9	50	4		45	50	G1-L		8
J-3	LP1	PNP	2	4	9	50	4		60	50	G1-L		4
JP-1	LP1	PNP	2	4	19	0	10		8	50	G1-L		4
L-5021	LP8	PNP	2	4	11	50	4		60	50	G1-L		19
L-5022	LP8	PNP	2	4	12	31	4		38	50	G1-L		10
L-5108	LP8	PNP	2	4	8	50	4		9	50	G1-L		5
L-5116	LP8	PNP	2	4	6	50	4		6	50	G1-L	9	8
L-5122	LP8	PNP	2	4	8	50	4		9	50	G1-L		8
LT-5021	P3	PNP	2	4	17	0	18		15	50	G2		10
LT-5022	P3	PNP	2	4	17	0	18		15	50	G2		10
LT-5023	P3	PNP	2	4	17	0	18		15	50	G2		20
LT-5024	P3	PNP	2	4	17	0	18		15	50	G2		20
LT-5025	P3	PNP	2	4	17	0	18		15	50	G2		20
LT-5026	P3	PNP	2	4	17	0	18		15	50	G2		30
LT-5027	P3	PNP	2	4	17	0	18		15	50	G2		30
LT-5028	P3	PNP	2	4	17	0	18		15	50	G2		30
LT-5029	P3	PNP	2	4	19	0	10		15	50	G2		20
LT-5030	P3	PNP	2	4	19	0	10		15	50	G2		20
LT-5031	P3	PNP	2	4	19	0	10		15	50	G2		20
LT-5032	P3	PNP	2	4	19	0	10		15	50	G2		30
LT-5033	P3	PNP	2	4	19	0	10		15	50	G2		30
LT-5034	P3	PNP	2	4	19	0	10		15	50	G2		30
LT-5035	P3	PNP	2	4	21	0	10		20	50	G2		10
LT-5036	P3	PNP	2	4	21	0	10		20	50	G2		10
LT-5037	P3	PNP	2	4	21	0	10		20	50	G2		20
LT-5038	P3	PNP	2	4	21	0	10		20	50	G2		20
LT-5039	P3	PNP	2	4	21	0	10		20	50	G2		20
LT-5040	P3	PNP	2	4	21	0	10		20	50	G2		30
LT-5041	P3	PNP	2	4	21	0	10		20	50	G2		30
LT-5042	P3	PNP	2	4	21	0	10		20	50	G2		30
LT-5043	P3	PNP	2	4	21	0	10		26	50	G2		10
LT-5044	P3	PNP	2	4	21	0	10		26	50	G2		10
LT-5045	P3	PNP	2	4	21	0	10		26	50	G2		10
LT-5046	P3	PNP	2	4	21	0	10		26	50	G2		20
LT-5047	P3	PNP	2	4	21	0	10		26	50	G2		20
LT-5048	P3	PNP	2	4	21	0	10		26	50	G2		20
LT-5049	P3	PNP	2	4	21	0	10		26	50	G2		30
LT-5050	P3	PNP	2	4	21	0	10		26	50	G2		30

Continued on Next Page

Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	ICBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
LT-5051	P3	PNP	2	4	21	0	10		26	50	G2	30	
LT-5052	P3	PNP	2	4	17	0	22		20	50	G2	15	
LT-5053	P3	PNP	2	4	17	0	22		20	50	G2	15	
LT-5054	P3	PNP	2	4	17	0	22		20	50	G2	15	
LT-5055	P3	PNP	2	4	17	0	22		20	50	G2	30	
LT-5056	P3	PNP	2	4	17	0	22		20	50	G2	30	
LT-5057	P3	PNP	2	4	17	0	22		20	50	G2	30	
LT-5058	P3	PNP	2	4	17	0	22		20	31	G2	25	
LT-5059	P3	PNP	2	4	17	0	22		20	31	G2	25	
LT-5060	P3	PNP	2	4	17	0	22		20	31	G2	25	
LT-5061	P3	PNP	2	4	19	0	12		20	50	G2	15	
LT-5062	P3	PNP	2	4	19	0	12		20	50	G2	15	
LT-5063	P3	PNP	2	4	19	0	12		20	50	G2	15	
LT-5064	P3	PNP	2	4	19	0	12		20	50	G2	30	
LT-5065	P3	PNP	2	4	19	0	12		20	50	G2	30	
LT-5066	P3	PNP	2	4	19	0	12		20	50	G2	30	
LT-5067	P3	PNP	2	4	19	0	12		20	31	G2	25	
LT-5068	P3	PNP	2	4	19	0	12		20	31	G2	25	
LT-5069	P3	PNP	2	4	19	0	12		20	31	G2	25	
LT-5070	P3	PNP	2	4	20	0	12		26	50	G2	15	
LT-5071	P3	PNP	2	4	20	0	12		26	50	G2	15	
LT-5072	P3	PNP	2	4	20	0	12		26	50	G2	15	
LT-5073	P3	PNP	2	4	20	0	12		26	50	G2	30	
LT-5074	P3	PNP	2	4	20	0	12		26	50	G2	30	
LT-5075	P3	PNP	2	4	20	0	12		26	50	G2	30	
LT-5076	P3	PNP	2	4	20	0	12		26	31	G2	25	
LT-5077	P3	PNP	2	4	20	0	12		26	31	G2	25	
LT-5078	P3	PNP	2	4	20	0	12		26	31	G2	25	
LT-5079	P3	PNP	2	4	21	0	12		30	50	G2	15	
LT-5080	P3	PNP	2	4	21	0	12		30	50	G2	15	
LT-5081	P3	PNP	2	4	21	0	12		30	50	G2	15	
LT-5082	P3	PNP	2	4	21	0	12		30	50	G2	30	
LT-5083	P3	PNP	2	4	21	0	12		30	50	G2	30	
LT-5084	P3	PNP	2	4	21	0	12		30	50	G2	30	
LT-5085	P3	PNP	2	4	21	0	12		30	31	G2	25	
LT-5086	P3	PNP	2	4	21	0	12		30	31	G2	25	
LT-5087	P3	PNP	2	4	21	0	12		30	31	G2	25	
LT-5088	P3	PNP	2	4	17	0	30		30	50	G2	20	
LT-5089	P3	PNP	2	4	17	0	30		30	50	G2	20	
LT-5090	P3	PNP	2	4	17	0	30		30	50	G2	20	
LT-5091	P3	PNP	2	4	17	0	30		30	31	G2	20	
LT-5092	P3	PNP	2	4	17	0	30		30	31	G2	20	
LT-5093	P3	PNP	2	4	17	0	30		30	31	G2	20	
LT-5094	P3	PNP	2	4	17	0	30		30	31	G2	40	
LT-5095	P3	PNP	2	4	17	0	30		30	31	G2	40	
LT-5096	P3	PNP	2	4	17	0	30		30	31	G2	40	
LT-5097	P3	PNP	2	4	19	0	18		30	50	G2	20	
LT-5098	P3	PNP	2	4	19	0	18		30	50	G2	20	
LT-5099	P3	PNP	2	4	19	0	18		30	50	G2	20	
LT-5100	P3	PNP	2	4	19	0	18		30	31	G2	20	
LT-5101	P3	PNP	2	4	19	0	18		30	31	G2	20	
LT-5102	P3	PNP	2	4	19	0	18		30	31	G2	20	
LT-5103	P3	PNP	2	4	19	0	18		30	31	G2	40	

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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test Reject Above:	ICBO		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row				Typ	Reject Above:		Test	Typ	Reject Below:
LT-5104	P3	PNP	2	4	19	0	18		30	31	G2		40
LT-5105	P3	PNP	2	4	19	0	18		30	31	G2		40
LT-5106	P3	PNP	2	4	20	0	16		36	50	G2		20
LT-5107	P3	PNP	2	4	20	0	16		36	50	G2		20
LT-5108	P3	PNP	2	4	20	0	16		36	50	G2		20
LT-5109	P3	PNP	2	4	20	0	16		36	31	G2		20
LT-5110	P3	PNP	2	4	20	0	16		36	31	G2		20
LT-5111	P3	PNP	2	4	20	0	16		36	31	G2		20
LT-5112	P3	PNP	2	4	20	0	16		36	31	G2		40
LT-5113	P3	PNP	2	4	20	0	16		36	31	G2		40
LT-5114	P3	PNP	2	4	20	0	16		36	31	G2		40
LT-5115	P3	PNP	2	4	21	0	14		40	50	G2		20
LT-5116	P3	PNP	2	4	21	0	14		40	50	G2		20
LT-5117	P3	PNP	2	4	21	0	14		40	50	G2		20
LT-5118	P3	PNP	2	4	21	0	14		40	31	G2		20
LT-5119	P3	PNP	2	4	21	0	14		40	31	G2		20
LT-5120	P3	PNP	2	4	21	0	14		40	31	G2		20
LT-5121	P3	PNP	2	4	21	0	14		40	31	G2		40
LT-5122	P3	PNP	2	4	21	0	14		40	31	G2		40
LT-5123	P3	PNP	2	4	21	0	14		40	31	G2		40
LT-5160	P3	PNP	2	4	21	0	14		40	50	G2		20
LT-5161	P3	PNP	2	4	21	0	14		40	50	G2		20
LT-5162	P3	PNP	2	4	21	0	14		40	50	G2		20
LT-5163	P3	NPN	2	4	17	18	34		60	50	G2		15
LT-5163L	P3	NPN	2	4	17	18	34		60	50	G2		15
LT-5164	P3	NPN	2	4	17	18	34		60	50	G2		15
LT-5164L	P3	NPN	2	4	17	18	34		60	50	G2		15
LT-5165	P3	NPN	2	4	17	18	34		60	50	G2		15
MN-13A	LP2	PNP	2	4	17	18	12		36	50	G1-L		5
MN-13B	LP2	PNP	2	4	17	18	12		36	50	G1-L		10
MN-13C	LP2	PNP	2	4	17	18	12		36	50	G1-L		20
MN-19	LP2	PNP	2	4	13	50	4		30	50	G1-L		12
MN-21	P-3	PNP	2	4	20	0	18		30	50	G2		20
MN-24	P3	PNP	2	4	18	0	30		30	50	G2		10
MN-25	P3	PNP	2	4	18	0	30		30	50	G2		12
MN-26	P3	PNP	2	4	18	0	30		30	50	G2		17
MN-28	P3	PNP	2	4	17	0	44		50	50	G2		15
MN-29	P3	PNP	2	4	17	0	44		50	50	G2		15
MN-32	P3	PNP	2	4	17	0	30		30	50	G2		15
OC-16	P10	PNP	2	4	13	18	8		60	50	G2		10
OC-44	LP1	PNP	2	4	13	50	4		30	50	G1-L		22
OC-45	LP1	PNP	2	4	13	50	4		30	50	G1-L		12
OC-65	LP5	PNP	2	4	8	50	4		36	50	G1-L		10
OC-66	LP5	PNP	2	4	8	50	4		36	50	G1-L		15
OC-70	LP1	PNP	2	4	8	50	4		36	50	G1-L		9
OC-71	LP1	PNP	2	4	8	50	4		36	50	G1-L		14
OC-72	LP1	PNP	2	4	11	50	4		30	50	G1-L		12
OC-73	LP1	PNP	2	4	17	50	4		27	50	G1-L		15
OC-76	LP1	PNP	2	4	11	50	4		30	50	G1-L		7
RD-316*	LP1	NPN	2	4	17	50	4		30	50	G1-L	9	3

*Maximum Emitter to Base Voltage rating = 1 Volt, therefore DO NOT use more than 1 volt to measure IEBO.

RF-1	LP1	PNP	2	4	9	50	4		30	50	G1-L		8
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Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
S-500	LP9	PNP	2	4	11	50	2		2	50	G1-L	18	4
S-501	LP9	PNP	2	4	11	50	2		2	50	G1-L	18	4
SB-100	LP8	PNP	2	4	8	50	4		9	50	G1-L		6
SB-101	LP8	PNP	2	4	8	50	4		9	50	G1-L		5
SB-102	LP8	PNP	2	4	8	50	4		9	50	G1-L		12
SB-103	LP8	PNP	2	4	8	50	4		9	50	G1-L		5
SB-5122	LP8	PNP	2	4	8	50	4		9	50	G1-L		8
ST-10	LP9	NPN	2	4	13	50	2		2	50	G1-L		10
ST-11	LP9	NPN	2	4	13	50	2		2	50	G1-L		5
ST-12	LP9	NPN	2	4	13	50	2		2	50	G1-L		20
ST-13	LP9	NPN	2	4	13	50	2		2	50	G1-L		15
ST-30	LP9	NPN	2	4	17	50	2		2	50	G1-L		5
ST-31	LP9	NPN	2	4	17	50	2		2	50	G1-L		10
ST-32	LP9	NPN	2	4	17	50	2		2	50	G1-L		20
ST-33	LP9	NPN	2	4	17	50	2		2	50	G1-L		15
ST-40	LP9	NPN	2	4	19	50	2		2	50	G1-L		5
ST-41	LP9	NPN	2	4	19	50	2		2	50	G1-L		10
ST-42	LP9	NPN	2	4	19	50	2		2	50	G1-L		20
ST-903	LP1	NPN	2	4	17	50	4		30	50	G1-L		4
ST-904	LP1	NPN	2	4	17	50	4		30	50	G1-L		9
ST-904A	LP1	NPN	2	4	17	50	4		30	50	G1-L		9
ST-905	LP1	NPN	2	4	17	50	4		30	50	G1-L		18
ST-910	LP1	NPN	2	4	17	50	4		30	50	G1-L		38
ST-1026	LP2	NPN	2	4	6	50	4		30	50	G1-L		8
SYL-1107	LP9	NPN	2	4	17	18	4		45	50	G1-L		7
T-0031	LP8	PNP	2	4	19	50	4		45	50	G1-L		17
T-1000	LP8	PNP	2	4	11	50	4		60	50	G1-L		22
T-1001	LP8	PNP	2	4	11	50	4		60	50	G1-L		35
T-1025	LP8	PNP	2	4	11	50	2		2	50	G1-L		4
T-1040	P6	PNP	2	4	11	11	14		75	50	G2		15
T-1041	P6	PNP	2	4	11	11	14		75	50	G2		20
T-1050	LP8	PNP	2	4	6	50	4		6	50	G1-L	9	
T-1159	LP8	PNP	2	4	11	50	2		2	50	G1-L		4
T-1166	LP8	PNP	2	4	8	50	4		15	50	G1-L		10
T-1167	P6	PNP	2	4	20	0	36		50	50	G2		10
T-1168	P6	PNP	2	4	20	0	30		10	50	G2		10
T-1392	LP2*	PNP	2	4	19	18	4		45	50	G2		20
*Lead No. 1 connected to case, keep transistor from touching instrument panel.													
T-1396	LP2*	PNP	2	4	19	18	4		45	50	G1-L		20
*Lead No. 1 connected to case, keep transistor from touching instrument panel.													
T-1397	LP2*	PNP	2	4	19	18	4		45	50	G1-L		25
*Lead No. 1 connected to case, keep transistor from touching instrument panel.													
T-1398	LP2*	PNP	2	4	19	18	4		45	50	G2		25
*Lead No. 1 connected to case, keep transistor from touching instrument panel.													
T-1546	LP2	PNP	2	4	17	31	4		38	31	G1-L		25
TR-10	LP2	PNP	2	4	19	50	4		25	50	G1-L		18
TR-19	LP2	PNP	2	4	9	50	8	6		31	G1-L	50	
TR-87	LP2	PNP	2	4	13	50	8	15		50	G1-L	28	
TR-88	LP2	PNP	2	4	13	50	8	15		31	G1-L	32	

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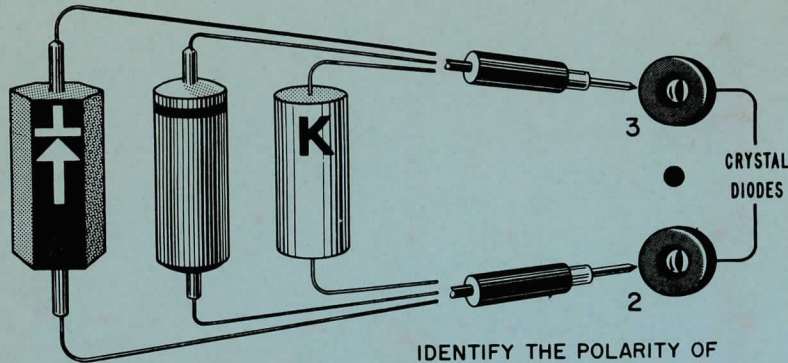
Transistor	See Fig. No.	NPN or PNP	Lever Settings		"B" Sw Setting	"C" Cont. Setting For Icbo	Short Test	Icbo		"C" Cont. Setting For Gain	Gain		
			"Base" Row	"Collector" Row			Reject Above:	Typ	Reject Above:		Test	Typ	Reject Below:
TR-758A	LP2	PNP	2	4	13	50	4	5		50	G1-L	15	
TR-761	LP2	PNP	2	4	9	50	4	2		31	G1-L	37	
TR-762	LP2	PNP	2	4	9	50	4	2		31	G1-L	37	
TR-763	LP2	PNP	2	4	8	50	4	2		15	G1-L	40	
TR-764	LP2	PNP	2	4	13	50	4	5		15	G1-L	40	
TS-161	LP5	PNP	2	4	11	50	6	8		50	G1-L	49	
TS-162	LP5	PNP	2	4	11	50	10	15		50	G1-L	16	
TS-163	LP5	PNP	2	4	17	50	8	25		50	G1-L	24	
TS-164	LP5	PNP	2	4	17	50	8	25		50	G1-L	44	
TS-165	LP5	PNP	2	4	17	50	8	25		50	G1-L	90	
TS-166	LP5	PNP	2	4	11	50	10	15		50	G1-L	39	
TS-176	P3	PNP	2	4	17	0	14		10	50	G2		15
TS-612	P3	PNP	2	4	17	11	10		90	50	G2		10
TS-613	P3	PNP	2	4	17	11	10		90	50	G2		10
TS-614	P3	PNP	2	4	17	11	10		90	50	G2		15
TS-615	LP1	PNP	2	4	19	50	4		45	50	G1-L	25	
TS-616	LP2	PNP	2	4	17	50	4		60	50	G1-L		10
TS-617	LP2	PNP	2	4	17	50	4		60	50	G1-L		30
TS-618	LP2	PNP	2	4	17	50	4		60	50	G1-L		30
TS-619	LP1	PNP	2	4	17	50	4		45	50	G1-L		16
TS-620	LP1	PNP	2	4	17	50	4		45	50	G1-L	50	
TS-621	LP1	PNP	2	4	17	50	4		45	31	G1-L	50	
TS-630	LP1	PNP	2	4	19	50	4		45	50	G1-L	50	
X-30	P11	NPN	2	3	19	31	4		38	50	G2	6	2
X-31	P11	NPN	2	3	21	31	4		38	50	G2	6	2
X-32	P11	NPN	2	3	21	31	4		38	50	G2	6	2
X-55	LP2	PNP	2	4	13	50	4		30	50	G1-L		10
X-56	LP2	PNP	2	4	13	50	4		30	50	G1-L		10
X-110	P3	PNP	2	4	19	0	14		15	50	G2		20
X-113	P3	PNP	2	4	20	0	38		80	50	G2		10
X-134	P3	PNP	2	4	17	0	42		40	50	G2		5
XH-10	P1*	PNP	2	4	20	0	34		70	50	G2		10

*Terminals stamped on transistor—Connect E to 1, B to 2, C to 4.

ZJ7-1	LP11	NPN	2-3†	4	10	31	6		38	31	G1-L	50	
†Tetrode transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													
ZJ7-2	LP11	NPN	2-3†	4	10	31	6		38	31	G1-L	50	
†Tetrode transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													
ZJ7-3	LP11	NPN	2-3†	4	10	31	6		38	31	G1-L	50	
†Tetrode transistor—after Gain test, throw lever 3 to "Tetrode" row and re-read gain. Gain should decrease from previous reading.													

End of Transistor Test Data

CRYSTAL DIODE TEST DATA



IDENTIFY THE POLARITY OF
 CRYSTAL DIODE AND CONNECT TO 660 FOR TEST AS ABOVE.

To Read True Diode Reverse (Dr) Current in μa , Multiply Scale Readings By:	When "C" Control is set to:
1	50
2	31
5	18
10	11
200	0

Diode	"Df" Test			"Dr" Test			Diode	"Df" Test			"Dr" Test		
	"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Below:	"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Above:		"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Below:	"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Above:
1N21	"Microwave"—not testable—specs. available at operating freqs. only.						1N39A	2	29	25	21	11	40
1N21A	"Microwave"—not testable—specs. available at operating freqs. only.						1N39B	2	29	20	21	18	40
1N21B	"Microwave"—not testable—specs. available at operating freqs. only.						1N40	2	28	30	19	0	9
1N21C	"Microwave"—not testable—specs. available at operating freqs. only.						1N41	2	28	30	11	31	40
1N21D	"Microwave"—not testable—specs. available at operating freqs. only.						1N42	2	28	30	21	0	8
1N21Z	"Microwave"—not testable—specs. available at operating freqs. only.						1N43	2	29	25	8	50	40
1N22	"Microwave"—not testable—specs. available at operating freqs. only.						1N44	2	29	16	19	0	10
1N23	"Microwave"—not testable—specs. available at operating freqs. only.						1N45	2	29	16	19	11	82
1N23A	"Microwave"—not testable—specs. available at operating freqs. only.						1N46	2	29	16	19	0	15
1N23B	"Microwave"—not testable—specs. available at operating freqs. only.						1N47	2	29	20	21	11	100
1N23C	"Microwave"—not testable—specs. available at operating freqs. only.						1N48	2	29	20	19	0	8
1N23D	"Microwave"—not testable—specs. available at operating freqs. only.						1N49	2	29	20	15	11	40
1N23E	"Microwave"—not testable—specs. available at operating freqs. only.						1N50	2	29	20	15	11	40
1N25	"Microwave"—not testable—specs. available at operating freqs. only.						1N51	2	29	14	19	0	18
1N25A	"Microwave"—not testable—specs. available at operating freqs. only.						1N52	2	29	20	19	18	60
1N26	"Microwave"—not testable—specs. available at operating freqs. only.						1N53	"Microwave"—not testable—specs. available at operating freqs. only.					
1N27	"Microwave"—not testable—specs. available at operating freqs. only.						1N53A	"Microwave"—not testable—specs. available at operating freqs. only.					
1N28	"Microwave"—not testable—specs. available at operating freqs. only.						1N54	2	29	25	19	18	40
1N29	"Microwave"—not testable—specs. available at operating freqs. only.						1N54A	2	29	25	19	18	40
1N30	"Microwave"—not testable—specs. available at operating freqs. only.						1N55	2	29	14	21	11	60
1N31	"Microwave"—not testable—specs. available at operating freqs. only.						1N55	2	29	20	22	11	100
1N32	"Microwave"—not testable—specs. available at operating freqs. only.						1N55A	2	29	25	22	11	100
1N34	2	29	25	19	11	100	1N55B	2	29	55	18	11	60
1N34A	2	29	25	19	11	100	1N56	2	29	55	18	11	60
1N35	2	29	25	11	50	20	1N56A	2	29	55	18	11	60
	● Matched Pair—Test each diode separately						1N57	2	29	20	20	11	100
1N38	2	29	20	21	11	100	1N58	2	29	20	21	0	8
1N38A	2	29	20	21	0	6	1N58A	2	29	20	21	0	6
1N38B	2	29	20	21	11	40	1N59	2	29	16	22	0	8
1N39	2	29	20	22	11	65	1N59A	2	29	16	22	0	8
							1N60	2	29	16	17	0	8
							1N61	2	29	25	21	11	60

Diode	"Df" Test			"Dr" Test			Diode	"Df" Test			"Dr" Test			
	"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Below:	"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Above:		"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Below:	"B" Sw. Set.	"C" Cont. Set.	Reject Diode If Meter Reads Above:	
1N63	2	29	20	19	31	50	1N116A	2	29	45	19	18	40	
1N64	2	29	25	11	18	80	1N117	2	29	45	19	18	40	
1N65	2	29	14	19	11	40	1N117A	2	29	65	19	18	40	
1N66	2	29	25	19	0	8	1N118	2	29	65	19	18	40	
1N67	2	29	20	19	31	50	1N118A	2	29	78	19	18	40	
1N67A	2	29	20	19	31	50	1N119	2	29	25	19	31	70	
1N68	2	29	16	21	0	7	1N120	2	29	25	19	18	56	
1N68A	2	29	16	21	0	7	1N126	2	29	25	19	0	9	
1N69	2	29	25	19	0	8	1N126A	2	29	25	19	0	9	
1N69A	2	29	25	19	11	100	1N127	2	29	16	19	11	60	
1N70	2	29	16	19	11	60	1N127A	2	29	16	19	11	60	
1N70A	2	29	16	19	11	60	1N128	2	29	16	12	50	20	
1N71	2	29	20	18	11	60	1N132	"Video det."—not testable—specifs. available at operating freqs. only.						
1N73	2	28	36	11	31	50	1N137A	2	29	16	15	50	2	
1N7	2	28	36	11	31	50	1N137B	2	29	65	15	50	2	
1N75	"Microwave"—not testable—specifs. available at operating freqs. only.							1N138A	2	29	25	11	50	2
1N76	"Microwave"—not testable—specifs. available at operating freqs. only.							1N138B	2	29	78	11	50	2
1N78	"Microwave"—not testable—specifs. available at operating freqs. only.							1N139	2	29	65	19	0	15
1N78A	"Microwave"—not testable—specifs. available at operating freqs. only.							1N140	2	29	78	19	11	60
1N79	"Microwave"—not testable—specifs. available at operating freqs. only.							1N141	2	29	65	19	18	20
1N81	2	29	16	11	50	20	1N142	2	29	25	21	18	40	
1N81A	2	29	16	11	50	20	1N143	2	29	78	21	18	40	
1N82	"UHF"—not testable—specifications available at 470 to 890 MC.							1N144	3	17	100	15	11	40
1N82A	"UHF"—not testable—specifications available at 470 to 890 MC.							1N145	2	29	78	12	18	40
1N83	2	29	25	20	50	60	1N149	"Microwave"—not testable—specifs. available at operating freqs. only.						
1N86	2	29	20	19	0	8	1N150	"Microwave"—not testable—specifs. available at operating freqs. only.						
1N87	"Video det."—not testable—specifs. available at operating freqs. only.							1N172	"Microwave"—not testable—specifs. available at operating freqs. only.					
1N87A	"Video det."—not testable—specifs. available at operating freqs. only.							1N191	2	29	25	19	18	50
1N88	2	29	14	19	18	40	1N192	2	29	25	19	11	50	
1N89	2	29	18	19	18	40	1N198	2	29	20	11	31	75	
1N90	2	29	25	19	0	8	1N198A	2	29	20	19	31	50	
1N95	2	29	45	19	11	100	1N200	2	29	80	9	50	2	
1N96	2	29	65	19	11	100	1N201	2	29	75	11	50	2	
1N97	2	29	45	19	18	40	1N202	2	29	75	11	50	2	
1N97A	2	29	65	19	18	40	1N203	2	29	70	12	50	2	
1N98	2	29	65	19	18	40	1N204	2	29	59	13	50	2	
1N98A	2	29	78	19	18	40	1N205	2	29	49	14	50	2	
1N99	2	29	45	19	31	50	1N206	2	29	41	15	50	2	
1N99A	2	29	65	19	31	50	1N207	2	29	33	17	50	2	
1N100	2	29	65	19	31	50	1N208	2	29	27	18	50	2	
1N100A	2	29	78	19	31	50	1N209	2	29	22	19	50	2	
1N101	2	29	45	19	50	20	1N210	2	29	18	19	50	2	
1N102	2	29	55	17	50	6	1N211	2	29	16	19	50	2	
1N103	2	29	75	13	0	8	1N212	2	29	12	20	50	2	
1N104	2	29	75	13	0	8	1N251	2	29	12	15	50	40	
1N105	"Video det."—not testable—specifs. available at operating freqs. only.							1N252	2	29	22	8	50	2
1N106	2	29	65	21	18	28	1N265	2	29	16	19	11	60	
1N107	3	17	102	11	11	40	1N266	2	29	20	17	31	75	
1N108	2	29	80	19	11	40	1N267	2	29	18	12	31	50	
1N109	2	29	39	6	18	40	1N268	2	29	29	12	50	50	
1N111	2	29	25	19	18	50	1N270	3	17	104	19	18	40	
1N112	2	29	25	19	11	50	1N273	3	17	100	15	50	40	
1N113	2	29	14	19	18	50	1N276	2	29	78	19	18	40	
1N114	2	29	14	19	11	50	1N277	3	17	100	11	18	30	
1N115	2	29	14	19	0	5	1N278	2	29	65	19	18	50	
1N116	2	29	25	19	8	40	1N279	3	17	100	15	11	40	

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